

Deadlock detection of Java Bytecode

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Abstract

This paper presents a powerful technique for deadlock detection of Java programs. The technique uses typing rules for extracting infinite-state abstract models of the dependencies among the components of the Java intermediate language – the Java bytecode. Models are subsequently analysed by means of an extension of a solver that we have defined for detecting deadlocks in process calculi. Our technique is complemented by a prototype verifier that also covers most of the Java features.

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1 Introduction

Deadlocks are common flaws of concurrent programs that occur when a set of threads are blocked because each one is attempting to acquire a lock held by another one. Such errors are difficult to detect or anticipate, since they may not happen during every execution, and may have catastrophic effects for the overall functionality of the software system. At the time of writing this paper, the *Oracle Bug Database*¹ reports more than 40 unresolved bugs due to deadlocks, while the *Apache Issue Tracker*² reports around 400 unresolved deadlock bugs. These two databases refer to programs written in Java, a mainstream programming language in a lot of domains, such as web and cloud applications, user applications and mobile applications.

The objective of our research is to design and implement a technique capable of detecting potential deadlock bugs of Java programs *at compilation time*. This objective is difficult because Java has a complex concurrent model: it uses threads that may perform read/write operations over shared variables and whose execution depends on the scheduling strategy implemented in the Java Virtual Machine (JVM). In addition, Java, being a full-fledged programming language, includes an extensive standard library with lots of features implemented in native language.

To reduce the complexity of our work, we decided to address the Java bytecode, namely 198 instructions that are the compilation target of every Java application and have a reference semantics that is defined by the JVM behaviour. Therefore, it is possible to deliver correctness results without narrowing our original goal or oversimplifying our job. In fact, our technique is similar to those used for demonstrating the correctness of the Bytecode Verifier [19, 6, 15, 12].

In order to present our deadlock detection technique, we isolate a subset of Java bytecode, called $JVML_d$, which includes basic instructions for concurrency, such as thread creations, synchronizations, and creations of new objects. The language is defined in Section 3. The technique consists of two stages.

The first stage defines a type system that reconstructs the concurrent behaviour of methods. The key principles are the following ones. Each method has an associated type

¹ <http://bugs.java.com/>

² <https://issues.apache.org/jira>



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that depends on the type of the arguments (the object “`this`” is one argument) and that expresses the *concurrent behaviour*. This “concurrent behaviour” reports (i) the *sequence of locks* that has been acquired/released by the method, (ii) the *threads created*, and (iii) the *methods that have been invoked*. It includes the analysis of aliases that traces the creation of new objects and their copies (because `JVMLd` instructions may create and copy objects). The alias analysis is performed in a *symbolic way* by using a *finite set of names*: this is a critical part of our technique because methods may create threads and, when methods are either recursive or iterative, the set of created threads may be infinite. In particular, we had to devise finite representatives of (infinite sets of) thread names that are sound with respect to the (deadlock) analysis. Section 2 reports a code that can be written in (a simple extension of) `JVMLd` and that is problematic as regards deadlock detection. Section 4 describes the type system.

The second stage of our technique defines the analysis of the behavioural model. In fact, the three reports above – (i), (ii), and (iii) – are terms in a modelling language that extend so-called *lams* [11, 10, 14]. Lams are conjunctions and disjunctions of object dependencies and method invocations and the extension has been necessary for modelling *Java reentrant locks*. In particular, our dependencies also carry thread names – $(a, b)_t$ means that the thread t , which owns the lock of a , is going to lock b . In *Java*, the lam $(a, a)_t$ is not a circular dependency because it means that t is acquiring the lock of a *twice*. Because of this extension, the algorithm for detecting circularities in lams is different than the one in [10, 14]. We address this issue in Section 5.

Our deadlock detection technique has been prototyped and the verifier is called *JaDA*. While the type system in this paper simply checks static informations, *JaDA* infers the behavioural types from the bytecode (an overview of the algorithm is reported in [9]). Inference is important in practice because it lightens the analysis but, for space limitations, we decided to discuss it in the full paper. However, checking is crucial for type safety, which we address in a (very) concise way in Section 6 (for reviewer’s sake, the details appear in the Appendix). It is also worth to notice that *JaDA* includes several features of `JVML`; this has made possible to deliver initial assessments of the tool. Section 7 discusses *JaDA*, its assessments and related work; we conclude in Section 8.

2 Overview of `JVML` and of our technique

Figure 1 reports a *Java* class called `Network` and some of its `JVMLd` representation. The corresponding `main` method creates a network of `n` threads by invoking `buildNetwork` – say t_1, \dots, t_n – that are all potentially running in parallel with the caller – say t_0 . Every two adjacent threads share an object, which are also created by `buildNetwork`. Every thread t_i locks the two adjacent objects, that are passed as (implicit) arguments of the thread, and terminates – this is performed by the method `takeLocks`. It is well-known that, if the network is circular – the thread t_n is sharing one of its objects with t_0 and if all the threads have a symmetric strategy of locking objects then a deadlock may occur. On the contrary, if the network is not circular, no deadlock will ever occur. Therefore `buildNetwork(n, x, x)` is deadlocked (when $n > 0$) and `buildNetwork(n, x, y)` is deadlock free (assuming $x \neq y$).

The problematic issue of `Network` is that the number of threads is not known statically because `n` is an argument of `main`. This is displayed in the bytecode of `buildNetwork` in Figure 1 by the instruction at address 30 where a new thread is created and by the instruction at address 37 where the thread is started. The recursive invocation that causes the (static) unboundedness is found at instruction 47. Our technique is powerful enough to cope with such

```

class Network{
    public void main(int n){
        Object x = new Object();
        Object y = new Object();
        // buildNetwork(n, x, x); // deadlock
        buildNetwork(n, x, y); //no deadlock
    }
    public void buildNetwork(int n,
        Object x, Object y){
        if (n==0) {
            takeLocks(x,y) ;
        } else {
            final Object z = new Object() ;
            Thread t = new Thread(){
                public void run(){
                    takeLocks(x,z) ;
                } ;
            t.start();
            this.buildNetwork(n-1,z,y) ;
        }
    }
    public void takeLocks(Object x,
        Object y){
        synchronized(x){ synchronized(y){ } }
    }
}

public void buildNetwork(int n, Object x, Object y)
0  iload_1          //n
1  ifne 13
4  aload_0         //this
5  aload_2         //x
6  aload_3         //y
7  invokevirtual 24 //takeLocks(x, y):void
10 goto 50
13 new 3
16 dup
17 invokespecial 8 //Object()
20 astore 4        //z
22 new 26
25 dup
26 aload_0         //this
27 aload_2         //x
28 aload 4         //z
30 invokespecial 28 //Network$1(this, x, z)
33 astore 5        //thr
35 aload 5         //thr
37 invokevirtual 31 //start():void
40 aload_0         //this
41 iload_1         //n
42 iconst_1
43 isub
44 aload 4         //z
46 aload_3         //y
47 invokevirtual 36 //buildNetwork(n-1, z, y):void
50 return

```

■ **Figure 1** Java Network program and corresponding bytecode (only the `buildNetwork` method). Comments in the bytecode give information of the objects used and/or methods invoked in each instruction

problems and predict the faulty behaviour in case of the invocation `buildNetwork(n,x,x)` and the correct behaviour if the invocation is `buildNetwork(n,x,y)`. The technique works as follows. It infers abstract methods' behaviors by computing types, called *lams*, of their bytecode bodies. These lams abstract each bytecode instruction by dropping the *non-relevant* information for the deadlock analysis (e.g. operations on integer variables). In practice, the relevant operations for deadlock analysis are: locking operations (`monitorenter` and `monitorexit` instructions), thread spawning operations, function invocations and objects' structures. Thereafter the abstract model is analysed by a solver.

3 The language $JVML_d$

$JVML_d$ is a restriction of JVMML that includes basic constructs and instructions for concurrency³. In $JVML_d$, a program is a collection of *class files* whose methods have bodies written in $JVML_d$ bytecode. This bytecode is a partial map from *addresses* ADDR to instructions. Addresses, ranged over L, L', \dots , are intended to be nonnegative integers and we use the function $L + 1$ that returns the least address that is strictly greater than L . When P is a bytecode, we write $dom(P)$ to refer to its domain (the set of addresses) and we assume that $0 \in dom(P)$ for every bytecode P .

We use a number of *names*: for classes, ranged over by C, D, \dots , for fields, ranged over by f, f', \dots , for methods, ranged over by m, m', \dots , and for local variables, ranged over by x, y, \dots . A possible empty sequence of names or syntactic categories of the following grammar is written by over-lining the name or the syntactic category, respectively. For instance a

³ Actually, $JVML_d$ has a minor difference with respect to JVMML: in JVMML, local variables are addressed by non-negative integers instead of names.

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sequence of local variables is written \bar{x} . Class files CF are defined by the grammar:

$$\begin{aligned} CF & ::= \text{class } C \{ \text{fields} : \overline{FD} \text{ methods} : \overline{MD} \} & MD & ::= T \ m \ (C, \bar{T}) \ P \ P \\ FD & ::= C.f : T & T & ::= \top \mid \text{int} \mid C \end{aligned}$$

where \top is a special type that include all the other types (any value of any type has also type \top). This type will represent values that are unusable in our static semantics. The type name C represents a class type, *which is never recursive* in $JVML_d$.

Instructions $Instr$ of $JVML_d$ bytecode are of the following form:

$$\begin{aligned} Instr & ::= \text{inc} \mid \text{pop} \mid \text{push} \mid \text{load } x \mid \text{store } x \mid \text{if } L \mid \text{goto } L \\ & \mid \text{new } C \mid \text{putfield } C.f : T \mid \text{getfield } C.f : T \mid \text{monitorenter} \mid \text{monitorexit} \\ & \mid \text{invokevirtual } C.m(\bar{T}) \mid \text{start } C \mid \text{return} \end{aligned}$$

The informal meaning of these instructions is as follows:

- **inc** increments the content of the stack; **pop** and **push**, respectively, pops and pushes the integer 0 on the stack; **load** x and **store** x respectively loads the value of x on the stack and pops the top value of the stack by storing it in x ; **if** L pops the top value of the stack and either jumps to the instruction at address L , if it is nonzero, or goes to the next instruction; **goto** L is the unconditional jump;
- **new** C allocates a new object of type C , initializes it and pushes it on top of the stack; **putfield** $C.f : T$ pops the value on the stack and the underlying object value, and assigns the former to the field f of the latter; **getfield** $C.f : T$ pops the object on the stack and pushes the value in the field f of that object;
- **monitorenter**, **monitorexit** are the synchronization primitives that pop the object on the stack and respectively lock and unlock it;
- **invokevirtual** $C.m(T_1, \dots, T_n)$ pops n values from the stack (the arguments of the invocation) and dispatches the method m on the object on top of the stack; when the method terminates, the returned value is pushed on the stack;
- **start** C creates and starts a new thread for the object on top of the stack. This operation corresponds to `invokevirtual java/lang/Thread/start()` on a thread of class C in $JVML$. We separate it from **invokevirtual** in order to provide more structure to our semantics (because it has an effect on the set of threads – see the operational semantics in the Appendix, where we also consider the instruction **join**);
- **return** terminates program execution.

The bytecode in Figure 1 is written in a sugared extension of $JVML_d$. In particular, **aload** and **iload** correspond to our **load** instruction (when the argument is an object or an integer, respectively), **dup** duplicates the top of the stack, and **invokespecial** is the method invocation of the constructor of the class.

4 The static semantics

In this section we define the typing rules that are at the base of the analysis technique. In order to simplify the presentation and the notation, we decided to drop the effect analysis, which verifies that threads do not access to common objects in inconsistent ways (for example, one reading and the other one writing). In particular, we assume that fields are read-only as they cannot be modified after the initialisation (which is done by constructors that, in turn, are not concurrent). For reviewers' sake, the Appendix reports the complete analysis.

Type values, flattened record types, and record types.

The static semantics traces dependencies between objects by using *symbolic names*. We will use a set of *object names*, ranged over by a, b, \dots that includes *void* and the *thread names* (threads are objects in Java); when a name is a thread name, we use t, t', \dots . We also use x, y, z, \dots to range over (generic) *names* and X, Y, Z, \dots to range over *variable names* to be used in the typing of methods and to be instantiated when methods are invoked.

Let *type values* τ and *flattened record types* ϕ be the terms

$$\tau ::= \top \mid \text{int} \mid X \mid a \quad \phi ::= ([\mathbf{f} : \tau], \mathbf{C})$$

It is worth to remark that flattened record types also bear their class set, which is a singleton in JVM_d (but not in full Java, where sets may contain several classes because of inheritance). Types τ and ϕ are *unstructured* because fields are names, i.e. they are pointers to records whenever the corresponding type is a class. This expedient allows us to analyse *aliasing* in a simple way. However, the type system also uses *structured types* (e.g in method types). Let (*structured*) *record types* ρ be the terms (this definition is correct because JVM_d class types are *not recursive*):

$$\rho ::= \top \mid \text{int} \mid X \mid (a[\mathbf{f} : \rho], \mathbf{C}).$$

We shorten $a[\]$ into a ; henceforth *void* $[\]$ is shortened into *void*. Let $root(\cdot)$ be the function defined as follows: $root(\text{void}) = \varepsilon$, $root(a[\mathbf{f} : \rho]) = a$, $root(\cdot)$ is the identity otherwise.

Environments.

The type rules use *environments* Γ that map names to type values or to flattened record types. There are two basic operations on environments: one for *sequential composition* – the *update* $\Gamma[\Gamma']$ – and one for *parallel composition* – the *merge* $\Gamma + \Gamma'$. They are defined as follows

$$\Gamma[\Gamma'](a) = \begin{cases} \Gamma'(a) & \text{if } a \in \text{dom}(\Gamma') \\ \Gamma(a) & \text{otherwise} \end{cases} \quad (\Gamma + \Gamma')(a) = \begin{cases} \Gamma(a) & \text{if } a \in \text{dom}(\Gamma) \setminus \text{dom}(\Gamma') \\ \Gamma'(a) & \text{if } a \in \text{dom}(\Gamma') \setminus \text{dom}(\Gamma) \\ \Gamma(a) & \text{if } \Gamma(a) = \Gamma'(a) \end{cases}$$

For example, let $\Gamma = a \mapsto ([\mathbf{f} : b], \mathbf{C})$ and $\Gamma' = a \mapsto ([\mathbf{f} : c], \mathbf{C})$. Then $\Gamma[\Gamma'] = a \mapsto ([\mathbf{f} : c], \mathbf{C})$, which is the standard update of an environment (this is used in constructors), while $\Gamma + \Gamma'$ is undefined (because there is a *race condition* on the field \mathbf{f} of a).

There is a straightforward way to get an environment from a record type and, conversely, to transform an environment and an object name into its (structured) record type. This way is defined by $env(\cdot)$ and $mk_tree(\cdot)$ below:

$$env(\rho) = \begin{cases} \emptyset & \text{if } \rho \in \{\top, \text{int}, X\} \\ a \mapsto ([\mathbf{f} : root(\rho)], \mathbf{C}) + (\sum_{\rho' \in \bar{\rho}} env(\rho')) & \text{if } \rho = (a[\mathbf{f} : \rho], \mathbf{C}) \end{cases}$$

$$mk_tree(\Gamma, \tau) = \begin{cases} \tau & \text{if } \tau \in \{\top, \text{int}, X\} \\ (a[\mathbf{f} : mk_tree(\Gamma, \tau')], \mathbf{C}) & \text{if } \tau = a \text{ and } \Gamma(a) = ([\mathbf{f} : \tau'], \mathbf{C}) \end{cases}$$

(notice that $env(\cdot)$ is partial and it and $mk_tree(\cdot)$ are well defined as long as class types are *not recursive*, as it is indeed the case for JVM_d). We finally define $typeof(\Gamma, \tau) = \mathbf{C}$ if $\Gamma(\tau) = ([\mathbf{f} : \tau'], \mathbf{C})$; $typeof(\Gamma, \tau) = \text{int}$ if $\tau = \text{int}$; undefined otherwise. Similarly, for record types ρ .

In the following we will use sets of record types, noted $\mathcal{T}, \mathcal{R}, \dots$.

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```

Main(this | t,u) =  Object.init(x | t,u):x[] + Object.init(y | t,u):y[]
                  + buildNetwork(this,_,x,x | t,u) + buildNetwork(this,_,x,y | t,u)

takeLocks(this,x,y | t,u) = t:(u,x) & t:(x,y)

buildNetwork(this,_,x,y | t,u) =  takeLocks(this,x,y | t,u) + Object.init(z | t,u):z[]
                                + Network$1.init(t1,this,x,z | t, z):t1[this$0:this[], val$x:x[], val$z: z[]]
                                + Network$1.run(t1 | t1,u1)
                                + Network$1.run(t1 | t1,u1) & buildNetwork(this,_,z,y | t,u)

Object.init(this | t, u):this[] =  0

Network$1.init(this, x1, x2, x3 | t, u):this[this$0:x1, val$x:x2, val$z:x3] =  0

Network$1.run(this[this$0:x1, val$x:x2, val$z:x3] | t, u) =  takeLocks(x1, x2, x3 | t, u)

```

■ **Figure 2** Network's lams

Lams.

Behavioural types are *lams* [10], noted ℓ , whose syntax is

$$\ell ::= 0 \mid (a, b)_t \mid \mathbf{C.m}(\bar{\rho}) \rightarrow \rho' \mid (\nu a)\ell \mid \ell \& \ell \mid \ell + \ell$$

The type 0 is the empty type; $(a, b)_t$ specifies a dependency between the object a and the object b that has been created by the thread t . The term $\mathbf{C.m}(\bar{\rho}) \rightarrow \rho'$ defines the invocation of $\mathbf{C.m}$ with arguments $\bar{\rho}$ and with returned record type ρ' . The argument sequence $\bar{\rho}$ has always at least three elements in our case; in particular the first element is the record type of the carrier, while the last two elements of the tuple $\bar{\rho}$ are respectively the thread t that performed the invocation and the last object name whose lock has been acquired by t . These two informations are used by the analyzer to build the right dependencies between callers and callees. The operation $(\nu a)\ell$ creates a new name a whose scope is the type ℓ ; the operations $\ell \& \ell'$ and $\ell + \ell'$ define the conjunction and disjunction of the dependencies in ℓ and ℓ' , respectively. The operators $+$ and $\&$ are associative and commutative. We shorten $\ell_1 + \dots + \ell_n$ and $\ell_1 \& \dots \& \ell_n$ into $\sum_{i \in 1..n} \ell_i$ and $\&_{i \in 1..n} \ell_i$, respectively.

A *lam program* is a pair (\mathcal{L}, ℓ) , where \mathcal{L} is a *finite set of function definitions*

$$\mathbf{C.m}(\bar{\rho}) \rightarrow \rho' = \ell_{\mathbf{C.m}}$$

with $\ell_{\mathbf{C.m}}$ being the *body* of $\mathbf{C.m}$, and ℓ is the *main lam*. We notice that the type ρ' is considered an argument of the lam function as well.

The lams of the Network's code in Figure 1 are shown in Figure 2 (lams have been simplified for easing the readability).

Behavioural Class Table.

A *behavioural class table* BCT is a map from pairs $\mathbf{C.m}$ to *method types* $(\bar{\rho}, t, a) \rightarrow (\nu \bar{a}') \langle \rho, \mathcal{T}, \mathcal{R}, \bar{\rho}', \ell \rangle$ where

- $\bar{\rho}$ is the tuple of record types of the carrier and of the arguments; t is the thread name of the caller and a is the last lock taken (and not released) by the caller;
- $(\nu \bar{a}')$ are the names that have been created by the method (and occur in the part in angular brackets);
- ρ is the record type of the returned value; this type may contain fresh object names that are created by the method body;
- \mathcal{T} is the set of threads that have been created by the method (this set only contains record types whose root names are fresh);

- \mathcal{R} is similar to \mathcal{T} . Because of recursion, $\mathbf{C.m}$ may create infinitely many threads; in this case \mathcal{R} is a finite representation of an infinite set;
- $\bar{\rho}'$ is the record type highlighting the changes to the arguments performed by the invocation (with the restriction we have, this is relevant for constructors);
- ℓ is the lam of the method $\mathbf{C.m}$.

Let σ be a substitution that renames object names and replaces variable names with record types and such that there is no clash of names \bar{c} with names in σ . The *instance of a method type* $(\bar{\rho}, t, a) \rightarrow (\nu \bar{c})\langle \rho, \mathcal{T}, \mathcal{R}, \bar{\rho}', \ell \rangle$ is

$$(\bar{\rho}, t, a)\sigma \rightarrow (\nu \bar{c})\langle \rho, \mathcal{T}, \mathcal{R}, \bar{\rho}', \ell \rangle \sigma$$

Let $(\bar{\rho}, t, a) \rightarrow (\nu \bar{c})\langle \rho, \mathcal{T}, \mathcal{R}, \bar{\rho}', \ell \rangle$ be an instance of $\text{BCT}(\mathbf{C.m})$. When we write $\text{BCT}(\mathbf{C.m})(\bar{\rho}, t, a)(\bar{b})$ we mean the tuple $\langle \rho, \mathcal{T}, \mathcal{R}, \bar{\rho}', \ell \rangle \{\bar{b}/\bar{c}\}$.

Typing judgments.

The judgment of JVM_d bytecode P will be $\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : \ell ; \Psi$ where:

- BCT is the behavioural class table; $\Gamma, F, S, Z, \mathcal{T}, \mathcal{R}$ are *vectors* indexed by the addresses in $\text{dom}(P)$;
- the environment Γ_i is the environment at address i ;
- the map F_i maps local variables to type values – we let F_{\top} be the map such that $F(x) = \top$, for every x ;
- S_i is a sequence of type values;
- Z_i is the *sequence of object names* locked at address i ;
- \mathcal{T}_i and \mathcal{R}_i are similar to the corresponding sets in method types;
- t is the symbolic thread name that executes P ;
- ℓ is *part of* the behavioural type of P at address i . The full behavioural type also includes Z_i, \mathcal{T}_i , and \mathcal{R}_i (see below);
- Ψ may be either empty, in this case the judgment is shortened into $\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : \ell$, or a tuple $(\rho, Z_i, \mathcal{T}_i, \mathcal{R}_i, \Gamma_i)$. Let \sqcup be the commutative and associative operator defined as follows

$$\begin{aligned} (\rho, Z_i, \mathcal{T}_i, \mathcal{R}_i, \Gamma_i) \sqcup \emptyset &\stackrel{\text{def}}{=} (\rho, Z_i, \mathcal{T}_i, \mathcal{R}_i, \Gamma_i) \\ (\rho, Z_i, \mathcal{T}_i, \mathcal{R}_i, \Gamma_i) \sqcup (\rho', Z_j, \mathcal{T}_j, \mathcal{R}_j, \Gamma_j) &\stackrel{\text{def}}{=} (\rho, Z_i, \mathcal{T}_i \cup \mathcal{T}_j, \mathcal{R}_i \cup \mathcal{R}_j, \Gamma_i + \Gamma_j) \end{aligned}$$

(\sqcup is only defined on tuples with the elements ρ and Z_i equal).

Let $Z_i = a_1 \cdots a_n$ be the sequence of (locked) object names at instruction i ; we assume that the leftmost object name is the more recent one that has been taken. The functions $[Z_i]$ and \widehat{Z}_i^t are defined as follows

$$[Z_i] = a_1 \quad \widehat{Z}_i^t = \&_{j \in 2..n} (a_j, a_{j-1})_t$$

We observe that Z_i may contain twice the same object name; this means that the thread has acquired twice the corresponding lock. For instance $Z_i = a \cdot a$. In this case $\widehat{Z}_i^t = (a, a)_t$, which is not a circular dependency – this is the reason why we index dependencies with thread names.

Let lock_t be a special object name that is paired with the thread t . The lam $\widehat{\mathcal{T}}_i$ is the term

$$\widehat{\mathcal{T}}_i = \&_{\rho \in \mathcal{T}_i} \text{typeof}(\rho). \text{run}(\rho, \mathbf{C}, t, \text{lock}_t)$$

Stack manipulation instructions

$$\begin{array}{c}
 \frac{P[i] = \mathbf{inc} \quad i+1 \in \text{dom}(P) \quad \Gamma_i = \Gamma_{i+1} \quad F_i = F_{i+1} \quad S_i = \mathbf{int} \cdot S' = S_{i+1} \quad Z_i = Z_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1}}{\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : 0} \\
 \\
 \frac{P[i] = \mathbf{pop} \quad i+1 \in \text{dom}(P) \quad \Gamma_{i+1} = \Gamma_i \quad F_i = F_{i+1} \quad S_i = \tau \cdot S_{i+1} \quad Z_i = Z_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1}}{\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : 0} \quad \frac{P[i] = \mathbf{push} \quad i+1 \in \text{dom}(P) \quad \Gamma_i = \Gamma_{i+1} \quad F_i = F_{i+1} \quad \mathbf{int} \cdot S_i = S_{i+1} \quad Z_i = Z_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1}}{\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : 0} \\
 \\
 \frac{P[i] = \mathbf{load} \ x \quad i+1 \in \text{dom}(P) \quad \Gamma_i = \Gamma_{i+1} \quad F_i = F_{i+1} \quad S_{i+1} = F_i(x) \cdot S_i \quad Z_i = Z_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1}}{\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : 0} \quad \frac{P[i] = \mathbf{store} \ x \quad i+1 \in \text{dom}(P) \quad \Gamma_i = \Gamma_{i+1} \quad F_{i+1} = F_i[x \mapsto \tau] \quad S_i = \tau \cdot S_{i+1} \quad Z_i = Z_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1}}{\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : 0}
 \end{array}$$

Control flow instructions

$$\frac{P[i] = \mathbf{goto} \ L \quad L \in \text{dom}(P) \quad \Gamma_i = \Gamma_L \quad F_i = F_L \quad S_i = S_L \quad Z_i = Z_L \quad \mathcal{T}_i = \mathcal{T}_L \quad \mathcal{R}_i = \mathcal{R}_{i+1}}{\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : 0} \quad \frac{P[i] = \mathbf{if} \ L \quad i+1, L \in \text{dom}(P) \quad \Gamma_{i+1} = \Gamma_i = \Gamma_L \quad F_i = F_{i+1} = F_L \quad S_i = \mathbf{int} \cdot S_{i+1} \quad S_{i+1} = S_L \quad Z_i = Z_{i+1} = Z_L \quad \mathcal{T}_i = \mathcal{T}_{i+1} = \mathcal{T}_L \quad \mathcal{R}_i = \mathcal{R}_{i+1} = \mathcal{R}_L}{\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : 0}$$

Return

$$\frac{P[i] = \mathbf{return} \quad S_i = \tau \cdot S'}{\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : 0 ; (mk_tree(\Gamma_i, \tau), Z_i, \mathcal{T}_i, \mathcal{R}_i, \Gamma_i)}$$

■ **Figure 3** Type rules for JVM_d programs – Part I

where `run` is the method that is invoked by the Java instruction `t.start()`. We notice that `start` has no arguments, except the callee `t`: the parameters are passed to the methods through the fields of `t`.

The lam $\widetilde{\mathcal{R}}_i$ is the term

$$\begin{aligned}
 \widetilde{\mathcal{R}}_i &= \&_{\rho \in \mathcal{R}_i} \text{RUN}(\rho) \\
 \text{RUN}((t[\bar{f} : \rho], C)) &= \mathbf{C.run}((t[\bar{f} : \rho], C), t, lock_t) \& (\nu t') \text{RUN}((t'[\bar{f} : \rho], C))
 \end{aligned}$$

The presence of recursion in $\widetilde{\mathcal{R}}_i$ is the main difference with $\widehat{\mathcal{T}}_i$. In fact, \mathcal{R}_i collects threads that have been created by recursive or iterative methods. This means that there may be several threads with equal fields but different root names running in parallel and that may compete for the same locks.

Whenever $i \in \text{dom}(P)$, we write “ i is recursive” if i is within a recursive or mutually recursive method. Otherwise we write “ i is not recursive”.

Type rules.

The type rules for a JVM_d programs are reported in Figures 3 and 4.

The function names(i). This function takes an address i and returns a tuple of names whose length depends on the address. It is the technical expedient to keep the set \mathcal{R} finite when methods are either recursive or iterative. For example, let $\mathbf{C.m}$ be a recursive method and let $(\bar{p}, t, a) \rightarrow (\nu a') \langle \rho, \mathcal{T}, \mathcal{R}, \bar{\rho}', \ell \rangle$ be its type. If $\mathbf{C.m}$ starts a new thread t and, in a successive instruction at address i , invokes itself recursively (this happens for instance in method `buildNetwork` of Figure 1) then the set \mathcal{R}_{i+1} will be equal to $\mathcal{R} \cup \{t\}$. If, in addition,

Method invocations

$$\begin{array}{c}
P[i] = \text{invokevirtual } \mathbf{C.m}(\mathbf{T}_0, \dots, \mathbf{T}_k) \quad i+1 \in \text{dom}(P) \quad S_i = \tau_k \cdots \tau_0 \cdot S' \\
\text{typeof}(\Gamma_i, \tau_0) = \mathbf{C} \quad \text{mk_tree}(\Gamma_i, (\tau_0, \dots, \tau_k)) = (\rho_0, \dots, \rho_k) \\
\bar{b} = \text{names}(i) \quad \bar{b} \cap \text{var}(\rho_0, \dots, \rho_k, t, [Z_i]) = \emptyset \\
\text{BCT}(\mathbf{C.m})(\rho_0, \dots, \rho_k, t, [Z_i])(\bar{b}) = \langle \rho, \mathcal{T}', \mathcal{R}', (\rho'_0, \dots, \rho'_k), \ell \rangle \\
\Gamma_{i+1} = \Gamma_i[\text{env}(\rho) + (+_{i \in 0..k} \text{env}(\rho'_i))] \\
S_{i+1} = \text{root}(\rho) \cdot S' \quad F_{i+1} = F_i \quad Z_{i+1} = Z_i \\
\mathcal{T}_{i+1}, \mathcal{R}_{i+1} = \begin{cases} \mathcal{T}_i \cup \mathcal{T}', \mathcal{R}_i \cup \mathcal{R}' & \text{if } i \text{ is not recursive and } (\mathcal{T}_i \cup \mathcal{R}_i) \cap (\mathcal{T}' \cup \mathcal{R}') = \emptyset \\ \mathcal{T}_i, \mathcal{R}_i \cup \mathcal{R}' \cup \mathcal{T}' & \text{otherwise} \end{cases} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : \mathbf{C.m}(\rho_0, \dots, \rho_k, t, [Z_i]) \rightarrow \rho \\
\\
P[i] = \text{start } \mathbf{C} \quad i+1 \in \text{dom}(P) \quad S_i = t' \cdot S_{i+1} \\
\text{typeof}(\Gamma_i, t') = \mathbf{C} \quad \text{mk_tree}(\Gamma_i, t') = \rho \quad \bar{b} = \text{names}(i) \quad \bar{b} \cap \text{var}(\rho, t', \text{lock}_{t'}) = \emptyset \\
\text{BCT}(\mathbf{C.run})(\rho, t', \text{lock}_{t'}) (\bar{b}) = \langle \text{void}, \mathcal{T}', \mathcal{R}', \rho', \ell \rangle \\
\Gamma_{i+1} = \Gamma_i + \text{env}(\rho') \quad F_i = F_{i+1} \quad Z_i = Z_{i+1} \\
\mathcal{T}_{i+1}, \mathcal{R}_{i+1} = \begin{cases} \mathcal{T}_i \cup \{\tau\} \cup \mathcal{T}', \mathcal{R}_i \cup \mathcal{R}' & \text{if } i \text{ is not recursive and } \tau \notin \mathcal{T}_i \cup \mathcal{R}_i \\ \mathcal{T}_i, \mathcal{R}_i \cup \mathcal{R}' \cup \mathcal{T}' \cup \{\tau\} & \text{otherwise} \end{cases} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : \mathbf{0}
\end{array}$$

Locking instructions

$$\begin{array}{c}
P[i] = \text{monitorenter} \quad i+1 \in \text{dom}(P) \\
\Gamma_{i+1} = \Gamma_i \quad F_i = F_{i+1} \quad S_i = a \cdot S_{i+1} \\
Z_{i+1} = a \cdot Z_i \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : \mathbf{0}
\end{array}
\qquad
\begin{array}{c}
P[i] = \text{monitorexit} \quad i+1 \in \text{dom}(P) \\
\Gamma_{i+1} = \Gamma_i \quad F_i = F_{i+1} \quad S_i = a \cdot S_{i+1} \\
Z_{i+1} = Z_i \setminus a \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : \mathbf{0}
\end{array}$$

Object manipulation instructions

$$\begin{array}{c}
P[i] = \text{new } \mathbf{C} \quad i+1 \in \text{dom}(P) \quad a = \text{names}(i) \quad \text{fields}(\mathbf{C}) = \bar{\mathbf{f}} \\
\Gamma_{i+1} = \Gamma_i[a \mapsto ([\bar{\mathbf{f}} : \bar{\mathbf{T}}], \mathbf{C})] \\
F_i = F_{i+1} \quad S_{i+1} = a \cdot S_i \quad Z_i = Z_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : \mathbf{0}
\end{array}$$

$$\begin{array}{c}
P[i] = \text{putfield } \mathbf{C.f} : \mathbf{T} \quad i+1 \in \text{dom}(P) \\
S_i = \tau \cdot a \cdot S_{i+1} \quad \Gamma_i(a) = ([\mathbf{f} : \tau', \bar{\mathbf{f}} : \tau'], \mathbf{C}) \\
\Gamma_{i+1} = \Gamma_i[a \mapsto ([\mathbf{f} : \tau, \bar{\mathbf{f}} : \tau'], \mathbf{C})] \quad F_i = F_{i+1} \\
Z_i = Z_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : \mathbf{0}
\end{array}
\qquad
\begin{array}{c}
P[i] = \text{getfield } \mathbf{C.f} : \mathbf{T} \quad i+1 \in \text{dom}(P) \\
S_i = a \cdot S' \quad \Gamma_i(a) = ([\mathbf{f} : \tau, \bar{\mathbf{f}} : \tau], \mathbf{C}) \\
\Gamma_{i+1} = \Gamma_i[a \mapsto ([\mathbf{f} : \tau, \bar{\mathbf{f}} : \tau], \mathbf{C})] \\
S_{i+1} = \tau \cdot S' \quad F_i = F_{i+1} \quad Z_i = Z_{i+1} \\
\mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : \mathbf{0}
\end{array}$$

Method definitions

$$\begin{array}{c}
\text{BCT}(\mathbf{C.m}) = (\rho_0, \dots, \rho_k, t, a) \rightarrow (\nu \bar{a}') \langle \rho, \mathcal{T}', \mathcal{R}', \bar{\rho}', \ell \rangle \quad F_1 = F_{\top}[0 \mapsto \text{root}(\rho_0), \dots, k \mapsto \text{root}(\rho_k)] \\
\Gamma_1 = +_{i \in 0..k} \text{env}(\rho_i) \quad S_1 = \varepsilon \quad Z_1 = a \quad \mathcal{T}_1 = \emptyset \quad \mathcal{R}_1 = \emptyset \\
\left(\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, i \vdash_t P : \ell_i ; \Psi_i \right)_{i \in \text{dom}(P)} \quad \ell = \sum_{i \in \text{dom}(P)} \widehat{\mathcal{T}}_i \ \& \ \widetilde{\mathcal{R}}_i \ \& \ \widehat{Z}_i^t \ \& \ \ell_i \\
\sqcup_{i \in \text{dom}(P)} \Psi_i = (\vartheta, a, \mathcal{T}', \mathcal{R}', \Gamma') \\
\bar{\rho}' = \text{mk_tree}(\Gamma', (\text{root}(\rho_0), \dots, \text{root}(\rho_k))) \quad \bar{a}' = \text{fn}(\rho, \mathcal{T}', \mathcal{R}', \bar{\rho}') \setminus \text{fn}(\rho_0, \dots, \rho_k, t, a) \\
\hline
\text{BCT} \vdash \mathbf{T.C.m}(\mathbf{T}_1, \dots, \mathbf{T}_k) P
\end{array}$$

■ **Figure 4** Type rules for JVM_{L_d} programs – Part II

the instruction at $i + 1$ is **return**, we obtain the equation $\mathcal{R} = \mathcal{R} \cup \{t\}$. This equation has no solution if t is always a fresh name (as it is the case in the JVM semantics); on the contrary it has solution $\mathcal{R} = \{t\}$ if $names(i) = t$. This second alternative has been our choice. In turn, this choice has an important impact on the analysis. In particular, we decided to constrain the threads created by recursive invocations or iterative codes to have the *same tree structure* – i.e. the corresponding nodes are equal. The analysis of behavioural types – the set \mathcal{R} is part of them – will take care of this over-approximation. Actually, the evaluation of \mathcal{R} spawns infinitely many lam functions with arguments bearing pairwise different fresh root names (cf. the function $\widetilde{\mathcal{R}}$), while it is not the case for the type of \mathcal{T} .

Comments about the type rules. The type rules that are important for the deadlock analysis are collected in Figure 4 and we will focus on them; the rules in Figure 3 are almost standard, see for instance [15].

The rule for **invokevirtual** computes the instance of $BCT(C.m)$ according to the arguments of the invocation. Let $\langle \rho, \mathcal{T}', \mathcal{R}', \overline{\rho'}, \ell \rangle$ be such instance. Since **invokevirtual** is a sequential invocation, its effects are reported in the environment Γ_{i+1} that types the next instruction. There are three effects: (i) the returned value, because it may be an object created by the method; (ii) the updates of the arguments of the invocation – the tuple $\overline{\rho'}$ – that, according to the restrictions we have, happens when $C.m$ is a constructor; (iii) the threads that have been spawned by $C.m$ – the set \mathcal{T}' , in case $C.m$ is not recursive, and the set \mathcal{R}' , in case $C.m$ is recursive or invokes methods that recursively spawn new threads. The sets \mathcal{T}_{i+1} and \mathcal{R}_{i+1} contain the threads that have been created by P up-to i . It is important to keep \mathcal{R}_{i+1} as small as possible because this impacts on the precision of our analysis: we are precise on threads in \mathcal{T}_{i+1} (because they are created exactly once) while we are over-approximate on threads in \mathcal{R}_{i+1} . So,

- if $C.m$ is not recursive and *the instruction i is not inside an iteration* (that is, it is executed once) then \mathcal{T}_{i+1} and \mathcal{R}_{i+1} are $\mathcal{T}_i \cup \mathcal{T}'$ and $\mathcal{R}_i \cup \mathcal{R}'$, respectively. The foregoing sentence in italics is expressed by the constraint $(\mathcal{T}_i \cup \mathcal{R}_i) \cap (\mathcal{T}' \cup \mathcal{R}') \neq \emptyset$. We explain this constraint with an example. Assume that $i + 1$ is a **goto i** (this means that the invocation is performed infinitely many times). Then, by the type rule for **goto**, $\mathcal{T}_i = \mathcal{T}_{i+1}$ and $\mathcal{R}_i = \mathcal{R}_{i+1}$. Therefore the threads in $\mathcal{T}' \cup \mathcal{R}'$ must be already in $\mathcal{T}_i \cup \mathcal{R}_i$.
- otherwise both \mathcal{T}' and \mathcal{R}' are added to \mathcal{R}_{i+1} .

The lam in the conclusion contains the invocation to $C.m$. We observe that this invocation is part of the behavioural type of the instruction: the full type also includes the terms $\widetilde{\mathcal{T}}_i$ & $\widetilde{\mathcal{R}}_i$ and the dependencies of the sequence of locks in Z_i – the term \widetilde{Z}_i^t – see the rule for method invocations.

The rule for **start** computes the instance of $BCT(C.run)$ where C is the type of the thread t' on top of the stack S_i . In our case $\rho' = \rho$ because fields cannot be updated. The environment Γ_{i+1} contains the threads spawned by $C.run$ – the sets \mathcal{T}' and \mathcal{R}' in the premise. The sets \mathcal{T}_{i+1} and \mathcal{R}_{i+1} are augmented with the thread that has been just spawned and those spawned by it as done with the rule for **invokevirtual**.

The rules for the locking instructions **monitorenter** and **monitorexit** update the map Z as expected. We notice that this update has an effect on the lam \widetilde{Z}_{i+1}^t by augmenting or reducing the dependencies, respectively.

The rule for method declarations $C.m$ verifies whether what is declared in $BCT(C.m)$ does match with what is checked by the judgments of the instructions in its body. Assuming that t is the name of the current thread and a is the last lock that has been acquired, then we constrain the first instruction of $C.m$ to be typed with F_1 such that the first k local variables

are set to the arguments of the invocation and Γ_1 defining such arguments. As regards S_1 , \mathcal{T}_1 and \mathcal{R}_1 , they are all empty, while $Z_1 = a$. The matching between $\text{BCT}(\mathbf{C.m})$ and what is checked by the judgments is performed by collecting the tuples $(\vartheta_i, Z_i, \mathcal{T}_i, \mathcal{R}_i, \Gamma_i)$ of the `return` statements and merging the results. Notice that, if $\mathbf{C.m}$ does not perform any concurrent operation (`start`, `monitorenter`, `monitorexit`) and does not perform method invocations then every \mathcal{T}_i is empty and every Z_i is a . Therefore, the lam of every instruction is always 0 .

5 The analysis of circularities in lams

Once behavioural types have been computed for the whole JVML_d program, we can analyse the type of the *main* method. The analysis uses an extension of the algorithm defined in [10, 14] that we briefly overview in this section. The paper [9] reports the pseudo-code of the algorithm that is implemented in our verifier; here we give an informal presentation.

The semantics of lams is very simple: it amounts to unfolding method invocations. The critical points are that (i) every invocation may create new fresh names and (ii) the method definitions may be recursive. These two points imply that a lam model may have infinite states, which makes any analysis nontrivial. It is worth to recall that the states of lams are conjunctions ($\&$) of dependencies and method invocation (because types with disjunctions $+$ are modelled by sets of states with conjunctive dependencies).

The results of [10, 14] allow us to reduce the analysis to *finite* models, *i.e.* finite disjunctions of finite conjunctions of dependencies. In turn, this finiteness makes possible to decide the presence of a so-called *circularities*, namely terms such as $(a, b)_t \& (b, a)_{t'}$.

The difference with [10, 14] is that there the dependencies are not indexed by thread names: here we use more informative dependencies in order to cope with Java reentrant locks. In particular $(a, b)_t \& (b, a)_t$ is not a circularity and, when $t \neq t'$, we carefully separate it from $(a, b)_t \& (b, a)_{t'}$. The definition of *transitive closure*, which is the base of the notion of circularity, is therefore new. Let $t \neq t'$ and let \checkmark be a special object name. Let also ℓ be a conjunction $\&$ of dependencies.

- The *transitive closure* of ℓ , noted ℓ^+ , is the least conjunction that contains ℓ and such that if $(a, b)_t \& (b, c)_{t'}$ is a subterm of ℓ^+ then either (i) $(a, c)_{\checkmark}$ is a subterm of ℓ^+ , if $t \neq t'$, or (ii) $(a, c)_t$ is a subterm of ℓ^+ , if $t = t'$.
- We say that ℓ has a *circularity* if there is a such that $(a, a)_{\checkmark}$ is a subterm of ℓ^+ .

For example $\ell = (a, b)_t \& (b, a)_t \& (b, c)_{t'}$ has no circularity because $\ell^+ = (a, b)_t \& (b, a)_t \& (a, a)_t \& (b, b)_t \& (b, c)_{t'} \& (a, c)_{\checkmark}$ does not contain any pair $(a, a)_{\checkmark}$ (this symbol \checkmark is a special thread name indicating that the dependency is due to the contributions of two or more threads).

► **Remark.** Actually, our verifier uses a notion of transitivity that is a refinement of the one described above. In particular, dependencies are labelled by the sequence of threads that contribute to them. These more informative labels allows us to compute transitive closures of terms like $(a, b)_t \& (b, c)_t \& (a, c)_{t'} \& (c, b)_{t'}$ in a more precise way. In fact, in this case, the mutual exclusion on the initial object a makes the concurrent presence of $(b, c)_t$ and $(c, b)_{t'}$ not possible. We finally notice that, using sequences of thread names as indices of dependencies allows us to reconstruct, at least in part, the execution path that produces the error. This is crucial for detecting false positives.

6 Intermezzo: correctness results

The proof of correctness of our type system is long, even if almost standard. In this section we overview it by highlighting the main parts; the details of this proof can be found in the Appendix. The first part of the proof addresses the soundness of the type system in Section 4. This requires

1. an operational semantics of $JVML_d$;
2. an extension of the type system to handle JVM runtime configurations.

Then, as usual with type systems, the soundness is represented by a subject reduction theorem expressing that, if a JVM configuration cn has lam ℓ and cn reduces to a configuration cn' then (i) cn' is also well-typed and (ii) if ℓ' is the type of cn' then ℓ and ℓ' are in a relation called *later stage* and noted $\ell \succcurlyeq \ell'$.

The second part of the proof is about the correctness of the later stage relation with respect to the deadlock analysis. We first demonstrate that the lam of a deadlocked configuration always contains a *circularity* between names. Then we demonstrate that, if $\ell \succcurlyeq \ell'$ and ℓ has *no circularity* then also ℓ' has no circular dependency.

As a byproduct of the above results we get that, if the lam of a $JVML_d$ program has no circularity then the $JVML_d$ program is deadlock-free.

7 Related Work and JaDA

Several techniques have been designed for detecting deadlocks of Java programs. We discuss separately those using static-time techniques and those requiring either model generation or runtime executions.

Static approaches

Type Systems. Few works use type systems for the deadlock detection in Java. To our knowledge, the most complete work is [4], which defines a type system that derives the order of lock acquisitions in *SafeJava* programs (a subset of Java with annotations written as part of the code). Well-typed programs will be verified deadlock free. An extension of this type system for JVM has been defined in [18]. Our technique differs from this approach in two key aspects. First of all, we aim at a fully automatic tool allowing to analyse existing programs without user intervention (or with a poor intervention that providing behavioural types of native methods because their code is not available). In addition, our types are behavioural; therefore deadlock-freedom is not a property of the type system, but it is verified by a solver that evaluates behavioural types.

Data Flow Analysis. A standard approach for detecting circular dependencies in concurrent programs is based on the construction of an execution flow graph and the search for cycles within this graph. In order to cope with aliasing (processes may use different names to access to the same resource), the definition of the execution flow graph uses a data-flow analysis technique, as in JLint [1]. In [20], data flow analysis is used for defining a formal set of rules that specify the lock order (the mechanism is similar to one used in the type system of this paper). As in our case, [20] has a sound strategy for detecting re-entrant locks; however the re-entrance in their case is restricted to lock expressions that only use local variables (it is not possible to use fields). Finally, the technique in [20] does not detect circularities with a common prefix (see Remark in Section 5), thus leading to a higher number of false positive outcomes. Data flow analysis is also used by Nayik [16] for verifying a set of necessary conditions for the existence of deadlocks. This is the theory used by Chord, one of

the most effective deadlock detection tools for Java according to [5, 16] (in the following we will compare **Chord** with our verifier).

Non-static approaches

Non static techniques usually analyse programs that have finite models. The advantage of these techniques is that they perform a very precise analysis.

Model Checking. [13] presents a model checking approach composed of two steps. A first step generates a so called *trace program* that records the critical concurrent operations and discards non critical parts. In a second step this program is analysed using an off-the-shelf model checker. Like this technique, our approach tries to get rid of non critical parts of the program. In our case, we use lams, thus allowing to perform the analysis in a reduced but key part of the program, without constraining the program model to be finite.

Monitoring Analysis. Monitoring techniques detect potential deadlocks at runtime. Our analysis of circularities has some similarities with the one presented in [3], such as the management of re-entrance. On the other hand the two approaches differ substantially when parallel codes is detected. Our technique detects the parallel states in the type system and, therefore, is over-approximate. The analysis of [3], being at runtime, tags each segment of the program that is reached by the execution flow by specify the exact order of lock acquisitions. Thereafter, they use an hybrid strategy for detecting potential deadlocks that might occur because of different scheduler choices (than the current one). The theory of [3] has been prototyped in the **GoodLock** tool, which has been used in the following assessment of our verifier. This technique has also been refined in **Sherlock**, which also uses symbolic executions [5].

JaDA

The technique described in this paper has been prototyped. The verifier, called **JaDA** [8, 9], is implemented in **Java** and includes features such as constructors, arrays, exceptions, static members, interfaces, inheritance, recursive data types (Garcia’s PhD thesis contains the technical details of these extensions [7]). These extensions have made possible to deliver an initial assessment of **JaDA** with respect to existing deadlock analysis tools for **Java**. In particular, we have taken **Chord** for static analysis [16], **Sherlock** for dynamic analysis [5], and **GoodLock** for hybrid analysis [3]. We have also considered a commercial tool, **ThreadSafe**⁴ [2]. Out of these tools, we were able to install and effectively test only two of them: **Chord** and **ThreadSafe**; the results corresponding to **GoodLock** and **Sherlock** come from [5]. We also had problems in testing **Chord** with some of the examples in the benchmarks, perhaps due to some misconfigurations, that we were not able to solve because **Chord** has been discontinued.

We have analysed a number of programs that exhibit a variety of sharing patterns. The source of all benchmarks in Table 1 is available either at [5, 16] or in the **JaDA-deadlocks** repository⁵. Since the current release of **JaDA** does not completely cover **JVM**, in order to gain preliminary experience, we modified the Java libraries and the multithreaded server programs of **RayTracer**, **MolDyn** and **MonteCarlo** (labelled with “(*)” in the Table 1) and implemented them in our system. This required little programming overhead; in particular, we removed volatile variables, avoided the use of **Runnable** interfaces for creating threads, and reduced the invocations of native methods involved in I/O operations.

⁴ <http://www.contemplateld.com/threadsafe>

⁵ <https://github.com/abelunibo/Java-Deadlocks>

■ **Table 1** Comparison with different deadlock detection tools. The inner cells show the number of deadlocks detected by each tool. The output labelled “(*)” are related to modified versions of the original programs: see the text.

benchmarks	Static		Hybrid	Dynamic	Commercial
	JaDA	Chord	GoodLock	Sherlock	ThreadSafe
Sor	1	1	7	1	4
RayTracer (*)	0	0	8	2	0
MolDyn (*)	0	0	6	1	0
MonteCarlo (*)	0	0	23	2	0
BuildNetwork	3	0			0
PhilosophersN	3	0			0
ThreadArrays	1	1			1
ThreadArraysWJoins	1	1			0
ScalaSimpleDeadlock	1				
ScalaPhilosophersN	3				

The first block of programs belongs to a well known group used as benchmarks for several Java analysis tools. In its current state JaDA only detects 1 deadlock in all of the four analysed programs from this group. It gives responses that are similar to ThreadSafe and Chord (ThreadSafe appears a bit more imprecise on Sor). The programs in the second block corresponds to examples designed to test our tool against complex deadlock scenarios like the Network program. We notice that both Chord and ThreadSafe fail to detect those kinds of deadlocks. The third group reports the analysis of two examples of Scala programs [17] (the Scala compiler 2.11 produces Java bytecode). We finally remark that, to the best of our knowledge, there is no static deadlock analysis tool for Scala (for this reason the entries corresponding to the other tools are empty).

8 Conclusions

We have defined a new technique for detecting deadlocks in Java programs by analysing the Java intermediate language JVMIL. The technique has been specified by focusing on a subset of JVMIL featuring thread creations and synchronizations, called JVMIL_d. We have also developed a prototype, called JaDA, which also covers complex features of Java, such as static members, arrays, recursive data types, exception handling, inheritance and dynamic dispatch. These extensions have made possible to deliver an initial assessment of JaDA with respect to existing deadlock analysis tools for Java.

Our future work includes the analysis of features of Java that have not yet been studied. One relevant feature is thread coordination, which is expressed by the methods `wait`, `notify` and `notifyAll`. The solution we are currently investigating uses more expressive *lams*, with special dependencies $(a, a^w)_t$ and $(a, a^n)_t$ for representing the wait-notify relations. In particular, $(a, a^w)_t$ means that “thread t has the lock a and has invoked the method `wait` on it”; $(a, a^n)_t$ means that “thread t has the lock a and has invoked the method `notify` on it”. These dependencies *and* the analysis that the wait operation *happens-before* the matching notification should increase the precision in detecting deadlocks.

Another extension addresses *native methods*, namely methods that are not implemented within the language and that are used when it is necessary to interact with the Operating System or for meta-programming purposes. Our current solution is to manually insert in the BCT the behavioural types of native methods. We are investigating testing mechanisms that may help in writing the types of such methods.

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A The full JVM_{L_d}

The language we address in the Appendix is actually an extension of the one presented in Section 3 because

1. we also consider the instruction `join C` that pops the thread on top of the stack and joins it with the current one. This operation corresponds to `invokevirtual java/lang/Thread/join()` on a thread of class `C` in JVM_{L_d};
2. we admit that methods update fields, not just constructors;
3. we admit `synchronized` methods.

These extensions (in particular the first two) will impact on the type system in a significative way because we need (i) to keep track of termination of threads and (ii) to analyze effects. In particular, as regards (ii), we must check data races of parallel threads (for example, one thread reading an object and the other one writing on the object) because such races reduce the precision of the analysis.

B JVM_{L_d} and its operational semantics

The instructions of JVM_{L_d} perform a transformation of machine states, called *configurations* and noted

$$\Vdash_H \langle \varphi_1, z_1 \rangle_{t_1} \cdots \langle \varphi_n, z_n \rangle_{t_n}$$

where H is the *heap* that contains objects created by `new` (see below) and a tuple $\langle \varphi, z \rangle_t$ represents an active thread t with:

- φ is a stack of activation records (or *frames*) (pc, f, s) with
 - pc is the *program counter* which contains the address of the instruction to be executed,
 - f is a total map from the set of local variables to the set of values,
 - s is the *operand Stack* of values;
- z is the set of objects locked by the thread.

The object in H contains:

- fields: the value of field f of object o is accessed with $H(o).f$; the update is noted $H(o).f \mapsto v$;
- the class of the object, returned by $H(o).class$;
- a counter z that records the number of locks performed by a thread on the object.

We use the notation $H(o) \mapsto (\rho_{\perp}^c, C)$ to allocate heap space for the object o of class C , by assigning a default value ρ_{\perp}^c to the fields and setting $H(o).z = 0$. The operational semantics of JVM_{L_d} is presented in Figures 5 and 6.

The *initial configuration* of a JVM_{L_d} program P with main method $C.main$ is $\Vdash_H \langle (1^{C.main}, f_{\perp}[0 \mapsto main], \epsilon), \epsilon \rangle_{main}$, where H defines the object *main* and f_{\perp} is the function that is always undefined.

In the following we will use \mathcal{C} to range over sets

$$\langle (pc^{C.m}, f_1, s_1) \cdot \varphi_1, z_1 \rangle_{t_1} \cdots \langle (pc^{D.m}, f_n, s_n) \cdot \varphi_n, z_n \rangle_{t_n}$$

and configurations will be ranged over by $\Vdash_H \mathcal{C}$.

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$$\begin{array}{c}
\text{(S-POP)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{pop}}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, v \cdot s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (\text{pc}^{\text{c.m.}} + 1, f, s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-LOAD)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{load } x}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (\text{pc}^{\text{c.m.}} + 1, f, f(x) \cdot s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-PUTFIELD)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{putfield } D.a \ \tau \quad H' = H(o).a \mapsto e}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, e \cdot o \cdot s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_{H'} \langle (\text{pc}^{\text{c.m.}} + 1, f, s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-PUSH)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{push}}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (\text{pc}^{\text{c.m.}} + 1, f, 0 \cdot s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-STORE)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{store } x}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, v \cdot s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (\text{pc}^{\text{c.m.}} + 1, f[x \mapsto v], s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-GETFIELD)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{getfield } D.a \ \tau \quad H(o).a = e}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, o \cdot s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (\text{pc}^{\text{c.m.}} + 1, f, e \cdot s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-INC)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{inc}}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, n \cdot s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (\text{pc}^{\text{c.m.}} + 1, f, (n + 1) \cdot s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-GOTO)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{goto } L^{\text{c.m.}}}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (L^{\text{c.m.}}, f, s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-NEW)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{new } D \quad o \notin \text{dom}(H) \quad H' = H[o \mapsto (\rho^D, D)]}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_{H'} \langle (\text{pc}^{\text{c.m.}} + 1, f, o \cdot s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-IF-TRUE)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{if } L^{\text{c.m.}} \ n \neq 0}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, n \cdot s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (L^{\text{c.m.}}, f, s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-IF-FALSE)} \\
\frac{P[\text{pc}^{\text{c.m.}}] = \text{if } L^{\text{c.m.}}}{P \Vdash_H \langle (\text{pc}^{\text{c.m.}}, f, 0 \cdot s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (\text{pc}^{\text{c.m.}} + 1, f, s) \cdot \varphi, z \rangle_t}
\end{array}$$

■ **Figure 5** Reduction rules: Basic operators

C Correctness

Let \mathcal{A} be the (infinite) set of *object names*, ranged over by a, b, c, \dots . In the syntax of ℓ , see Section 4, the operations “ $\&$ ” and “ $+$ ” are associative, commutative with 0 being the identity on $\&$, and definitions and lams are equal up-to alpha renaming of bound names. Namely, if $a \notin \text{var}(\ell)$, the following axioms hold:

$$(\nu a)\ell = \ell \quad ((\nu a)\ell')\&\ell = (\nu a)(\ell'\&\ell) \quad ((\nu a)\ell') + \ell = (\nu a)(\ell' + \ell)$$

Additionally, when V ranges over lams that do not contain function invocations, the following axioms hold:

$$V\&V = V \quad V + V = V \quad V\&(\ell' + \ell'') = V\&\ell' + V\&\ell'' \quad (1)$$

These axioms permit to rewrite a lam *without function invocations* as a *collection* (operation $+$) of *relations* (elements of a relation are gathered by the operation $\&$). Let \equiv be the least congruence containing the above axioms. (The axioms (1) are restricted to terms V that do not contain function invocations because $\mathfrak{m}(\bar{\rho}) \rightarrow \rho'\&((a, b)_t + (b, c)_t) \neq (\mathfrak{m}(\bar{\rho}) \rightarrow \rho'\&(a, b)_t) + (\mathfrak{m}(\bar{\rho}) \rightarrow \rho'\&(b, c)_t)$ because the evaluation of the two lams (see below) may produce terms with different names.)

Operational semantics.

Let a *lam context*, noted $L[\]$, be a term derived by the following syntax:

$$L[\] ::= [\] \quad | \quad \ell\&L[\] \quad | \quad \ell + L[\]$$

As usual $L[\ell]$ is the lam where the hole of $L[\]$ is replaced by ℓ . According to the syntax, lam contexts have no ν -binder; that is, the hassle of name captures is avoided. The operational

$$\begin{array}{c}
\text{(S-INVK)} \\
\frac{P[pc^{c.m}] = \text{invokevirtual } D.m'(T_1, \dots, T_n) \text{ synchronized } \notin \text{mod}(D.m')}{P \Vdash_H \langle (pc^{c.m}, f_1, v_n \cdots v_1 \cdot o \cdot s_1) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (1^{D.m'}, f_2[0 \mapsto o, 1 \mapsto v_1, \dots, n \mapsto v_n], \epsilon) \cdot (pc^{c.m} + 1, f_1, \bullet \cdot s_1) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-INVK-SYNCH-0)} \\
\frac{P[pc^{c.m}] = \text{invokevirtual } D.m'(T_1, \dots, T_n) \text{ synchronized } \in \text{mod}(D.m') \quad H(o).z = 0 \quad H^i = H(o).z \mapsto 1}{P \Vdash_H \langle (pc^{c.m}, f_1, v_n \cdots v_1 \cdot o \cdot s_1) \cdot \varphi, z \setminus \{o\} \rangle_t \rightarrow \Vdash_H \langle (1^{D.m'}, f_2[0 \mapsto o, 1 \mapsto v_1, \dots, n \mapsto v_n], \epsilon) \cdot (pc^{c.m} + 1, f_1, \bullet \cdot s_1) \cdot \varphi, z \cup \{o\} \rangle_t} \\
\\
\text{(S-INVK-SYNCH-N)} \\
\frac{P[pc^{c.m}] = \text{invokevirtual } D.m'(T_1, \dots, T_n) \text{ synchronized } \in \text{mod}(D.m') \quad H(o).z = n, n > 0 \quad H' = H(o).z \mapsto n + 1}{P \Vdash_H \langle (pc^{c.m}, f_1, v_n \cdots v_1 \cdot o \cdot s_1) \cdot \varphi, z \cup \{o\} \rangle_t \rightarrow \Vdash_{H'} \langle (1^{D.m'}, f_2[0 \mapsto o, 1 \mapsto v_1, \dots, n \mapsto v_n], \epsilon) \cdot (pc^{c.m} + 1, f_1, \bullet \cdot s_1) \cdot \varphi, z \cup \{o\} \rangle_t} \\
\\
\text{(S-START)} \\
\frac{P[pc^{c.m}] = \text{start } D}{P \Vdash_H \langle (pc^{c.m}, f_1, o \cdot s_1) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (pc^{c.m} + 1, f_1, s_1) \cdot \varphi, z \rangle_t, \langle (1^{D.run}, f_2[0 \mapsto o], \epsilon), \epsilon \rangle_o} \\
\\
\text{(S-JOIN)} \\
\frac{P[pc^{c.m}] = \text{join} \quad P[pc^{D.run}] = \text{return}}{P \Vdash_H \langle (pc^{c.m}, f_1, o \cdot s_1) \cdot \varphi_{t_1}, z_{t_1} \rangle_t, \langle (pc^{D.run}, f_2, s_2), z_{t_2} \rangle_o \rightarrow \Vdash_H \langle (pc^{c.m} + 1, f_1, s_1) \cdot \varphi_{t_1}, z_{t_1} \rangle_t, \langle (pc^{D.run}, f_2, s_2), z_{t_2} \rangle_o} \\
\\
\text{(S-RETURN)} \\
\frac{P[pc^{c.m}] = \text{return} \text{ synchronized } \notin \text{mod}(C.m)}{P \Vdash_H \langle (pc^{c.m}, f_1, v \cdot s_1) \cdot (pc^{D.m}, f_2, \bullet \cdot s_2) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle pc^{D.m}, f_2, v \cdot s_2 \rangle_t} \\
\\
\text{(S-RETURN-SYNCH-0)} \\
\frac{P[pc^{c.m}] = \text{return} \text{ synchronized } \in \text{mod}(C.m) \quad H(o).z = 1 \quad H' = H(o).z \mapsto 0}{P \Vdash_H \langle (pc^{c.m}, f_1, v \cdot s_1) \cdot (pc^{D.m}, f_2, \bullet \cdot s_2) \cdot \varphi, z \setminus \{o\} \rangle_t \rightarrow \Vdash_{H'} \langle (pc^{D.m}, v \cdot f_2, s_2) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-RETURN-SYNCH-N)} \\
\frac{P[pc^{c.m}] = \text{return} \text{ synchronized } \in \text{mod}(C.m) \quad H(o).z = n, n > 1 \quad H' = H(o).z \mapsto n - 1}{P \Vdash_H \langle (pc^{c.m}, f_1, v \cdot s_1) \cdot (pc^{D.m}, f_2, \bullet \cdot s_2) \cdot \varphi, z \cup \{o\} \rangle_t \rightarrow \Vdash_{H'} \langle (pc^{D.m}, f_2, v \cdot s_2) \cdot \varphi, z \cup \{o\} \rangle_t} \\
\\
\text{(S-MONITOREXIT-0)} \\
\frac{P[pc^{c.m}] = \text{monitorexit} \quad H(o).z = 1 \quad H' = H(o).z \mapsto 0}{P \Vdash_H \langle (pc^{c.m}, f, o \cdot s) \cdot \varphi, z \setminus \{o\} \rangle_t \rightarrow \Vdash_{H'} \langle (pc^{c.m} + 1, f, s) \cdot \varphi, z \rangle_t} \\
\\
\text{(S-MONITOREXIT-N)} \\
\frac{P[pc^{c.m}] = \text{monitorexit} \quad H(o).z = n, n > 1 \quad H' = H(o).z \mapsto n - 1}{P \Vdash_H \langle (pc^{c.m}, f, o \cdot s) \cdot \varphi, z \cup \{o\} \rangle_t \rightarrow \Vdash_{H'} \langle (pc^{c.m} + 1, f, s) \cdot \varphi, z \cup \{o\} \rangle_t} \\
\\
\text{(S-MONITORENTER-0)} \\
\frac{P[pc^{c.m}] = \text{monitorenter} \quad H = H(o).z = 0 \quad H' = H(o).z \rightarrow 1}{P \Vdash_H \langle (pc^{c.m}, f, o \cdot s) \cdot \varphi, z \setminus \{o\} \rangle_t \rightarrow \Vdash_{H'} \langle (pc^{c.m} + 1, f, s) \cdot \varphi, z \setminus \{o\} \rangle_t} \\
\\
\text{(S-MONITORENTER-N)} \\
\frac{P[pc^{c.m}] = \text{monitorenter} \quad H = H(o).z = n, n > 0 \quad H' = H(o).z \rightarrow n + 1}{P \Vdash_H \langle (pc^{c.m}, f, o \cdot s) \cdot \varphi, z \cup \{o\} \rangle_t \rightarrow \Vdash_{H'} \langle (pc^{c.m} + 1, f, s) \cdot \varphi, z \cup \{o\} \rangle_t}
\end{array}$$

■ **Figure 6** Reduction rules: Invocations and synchronizations

semantics of a program (\mathcal{L}, ℓ) is a transition system where *states* are lams, the *transition relation* is the least one satisfying the rule

$$\frac{\begin{array}{l} \mathfrak{m}(\bar{\rho}) \rightarrow \rho' = (\nu \bar{c})\ell_{\mathfrak{m}} \in \mathcal{L} \quad (\bar{\rho} \rightarrow \rho')\sigma = \bar{\rho}'' \rightarrow \rho''' \quad \bar{c}' \text{ fresh} \\ (\ell_{\mathfrak{m}}\{\bar{c}'/\bar{c}\})\sigma = \ell'_{\mathfrak{m}} \end{array}}{\mathbb{L}[\mathfrak{m}(\bar{\rho}'') \rightarrow \bar{\rho}'''] \rightarrow \mathbb{L}[\ell'_{\mathfrak{m}}]}$$

and the initial state is the lam ℓ' such that $\ell \equiv (\nu \bar{c})\ell'$ and ℓ' does not contain any ν -binder, as well as the lam $\ell_{\mathfrak{m}}$. (The class name in the names of lam functions has been dropped, for simplicity.) We write \rightarrow^* for the reflexive and transitive closure of \rightarrow .

By (RED), a lam ℓ is evaluated by successively replacing function invocations with the corresponding lam instances. Name creation is handled by replacing bound names of function bodies with fresh names.

Flattening and circularities

Informally, a lam has a *circularity* when it has a conjunction of dependencies whose transitive closure contains a pair $(a, a)_{\checkmark}$ (see Section 5). Here the hassle is that lams cannot be always rewritten in a form suitable for defining circularities, namely disjunctions of conjunctions, because the equations (1) do not apply to terms containing invocations. To overcome this problem, we define the operation of flattening. The transitive closure of a lam has been defined in Section 5.

► **Definition 1.** The *flattening* of a lam ℓ , noted $(\ell)^{\flat}$, is the lam ℓ where every function invocation has been replaced by $\mathbf{0}$.

For example, let $\ell = \mathfrak{m}(a, b, c, t) + (a, b)_t \& \mathfrak{m}'(b, c, t') \& \mathfrak{m}(d, b, c, t)$ (we assume that return types of lam functions are empty). Then $(\ell)^{\flat} = \mathbf{0} + (a, b)_t \& \mathbf{0} \& \mathbf{0}$.

► **Definition 2.** A lam ℓ has a *circularity* if $(\ell)^{\flat} \equiv \sum_{i \in I} \ell_i$, where every ℓ_i is a conjunction of dependencies, and, for some a and i , $(a, a)_{\checkmark}$ is a subterm of ℓ_i^+ .

A lam program (\mathcal{L}, ℓ) has a *circularity* if there exists ℓ' such that $\ell \rightarrow^* \ell'$ and ℓ' has a circularity.

Later stage relation

In the following subject reduction theorem we use a relation, called *later-stage relation* \succcurlyeq , which is the least congruence with respect to lams that contains the rules presented in Figure 7. The notation \succcurlyeq assumes the presence of a behavioural class table BCT – we should have noted the later-stage relation as $\succcurlyeq_{\text{BCT}}$, but we prefer to keep the BCT implicit. A relevant property of the later stage relation is the following.

► **Lemma 3** (Circularities). *Let $\ell \succcurlyeq \ell'$. If ℓ' has a circularity then ℓ has also a circularity.*

This result mostly follows from [10, 14].

C.1 Static-time typing

We rewrite the rules of static time typing. They are more verbose than those of Section 4 because we cover more features than there. However, the reader should easily retrieve the rules in the main paper by overlooking at the new items.

Since methods may update fields then parallel threads may manifest data races that jeopardise the precision of the analysis. This means that, in order to deliver a more precise

$$\begin{array}{c}
\begin{array}{ccc}
\text{(L-0)} & \text{(L-RES)} & \text{(L-PLUS)} & \text{(L-AND)} \\
\ell \geq 0 & \frac{\ell \geq \ell'}{(\nu a)\ell \geq (\nu a)\ell'} & \frac{\ell_1 \geq \ell'_1 \quad \ell_2 \geq \ell'_2}{\ell_1 + \ell'_1 \geq \ell_2 + \ell'_2} & \frac{\ell_1 \geq \ell'_1 \quad \ell_2 \geq \ell'_2}{\ell_1 \& \ell'_1 \geq \ell_2 \& \ell'_2}
\end{array} \\
\text{(L-INVK)} \\
\frac{\text{BCT}(\mathbf{C.m})(\bar{\rho}, t, a) = (\nu \bar{a}') \langle \rho, \mathcal{F}, \mathcal{R}, K, \bar{\rho}', \ell \rangle \quad \bar{b} \text{ fresh}}{\mathbf{C.m}(\bar{\rho}, t, a) \rightarrow (\rho \{\bar{b}/\bar{a}'\}) \geq \ell \{\bar{b}/\bar{a}'\}}
\end{array}$$

■ **Figure 7** The later-stage relation

analysis, we need to analyze effects of methods and verify their consistency when they run in parallel. For this reason, we annotate fields with *accesses* $\mathbf{h} = \{-, \mathbf{r}, \mathbf{w}\}$ – with $_$ meaning no access, \mathbf{r} meaning read access, \mathbf{w} meaning write (and possible read) access – and we let $- \leq \mathbf{r} \leq \mathbf{w}$. Since (\mathbf{h}, \leq) is a lattice, we will use the operation of least upper bound \sqcup . In the following, the *flattened record types* ς are terms where fields are labelled with accesses:

$$\tau ::= \top \mid \mathbf{int} \mid X \mid a \quad \varsigma ::= ([\mathbf{f}^{\mathbf{h}} : \tau], \mathbf{C})$$

With an abuse of notation, we will range over flattened record types with τ, τ', \dots . We always shorten \mathbf{f}^- into \mathbf{f} .

Let

$$\mathbf{f}^{\mathbf{h}} : \tau + \mathbf{f}^{\mathbf{h}'} : \tau' \stackrel{def}{=} \begin{cases} \mathbf{f}^{\mathbf{h} \sqcup \mathbf{h}'} : \tau & \text{if } \mathbf{h}, \mathbf{h}' \leq \mathbf{r} \text{ and } \tau = \tau' \\ \mathbf{f}^{\mathbf{w}} : \mathbf{int} & \text{if } \mathbf{h} \sqcup \mathbf{h}' = \mathbf{w} \text{ and } \tau = \mathbf{int} = \tau' \\ \mathbf{f}^{\mathbf{w}} : \top & \text{otherwise} \end{cases}$$

It is worth to notice that $\mathbf{f}^{\mathbf{h}} : X + \mathbf{f}^{\mathbf{h}'} : Y$ is \top when $X \neq Y$ (and similarly when $X = a$, $Y = b$ and $a \neq b$). Let also

$$([\dots, \mathbf{f}_i^{\mathbf{h}_i} : \tau_i, \dots], \mathbf{C}) + ([\dots, \mathbf{f}_i^{\mathbf{h}'_i} : \tau'_i, \dots], \mathbf{C}) \stackrel{def}{=} ([\dots, \mathbf{f}_i^{\mathbf{h}_i} : \tau_i + \mathbf{f}_i^{\mathbf{h}'_i} : \tau'_i, \dots], \mathbf{C})$$

(it is assumed that the fields of the records are the same – because they have the same class type – and the operation is performed on every field).

Environments.

The foregoing static semantics uses abstract heaps, called *environments* Γ , which map names to type values or to flattened record types. There are two basic operations on environments: one for *sequential composition* – the *update* $\Gamma[\Gamma']$ – and one for *parallel composition* – the *merge* $\Gamma + \Gamma'$. They are defined as follows

$$\begin{aligned}
\Gamma[\Gamma'](a) &= \begin{cases} \Gamma(a) & \text{if } a \in \text{dom}(\Gamma) \setminus \text{dom}(\Gamma') \\ \Gamma'(a) & \text{if } a \in \text{dom}(\Gamma') \setminus \text{dom}(\Gamma) \\ ([\dots, \mathbf{f}_i^{\mathbf{h}_i \sqcup \mathbf{h}'_i} : \tau_i, \dots], \mathbf{C}) & \text{if } \Gamma(a) = ([\dots, \mathbf{f}_i^{\mathbf{h}_i} : \tau_i, \dots], \mathbf{C}) \\ & \text{and } \Gamma'(a) = ([\dots, \mathbf{f}_i^{\mathbf{h}'_i} : \tau'_i, \dots], \mathbf{C}) \end{cases} \\
(\Gamma + \Gamma')(a) &= \begin{cases} \Gamma(a) & \text{if } a \in \text{dom}(\Gamma) \setminus \text{dom}(\Gamma') \\ \Gamma'(a) & \text{if } a \in \text{dom}(\Gamma') \setminus \text{dom}(\Gamma) \\ \Gamma(a) + \Gamma'(a) & \text{otherwise} \end{cases}
\end{aligned}$$

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For example, let $\Gamma = a \mapsto ([\mathbf{f}^h : b], \mathbf{C})$ and $\Gamma' = a \mapsto ([\mathbf{f}^w : c], \mathbf{C})$. Then $\Gamma[\Gamma'] = a \mapsto ([\mathbf{f}^w : c], \mathbf{C})$, which is the standard update of an environment plus the recording of the writing operation, while $\Gamma + \Gamma' = a \mapsto ([\mathbf{f}^w : \top], \mathbf{C})$, namely the field value is undefined because a *race condition* on the field \mathbf{f} of a caused by the parallel execution of two threads. (This case was not possible in Section 4 because of the restriction that fields are read-only.)

In the following we will also use record types with fields labelled with accesses. We will range over them with ρ, ρ', \dots and the syntax is

$$\rho ::= \top \mid \text{int} \mid X \mid (a[\overline{\mathbf{f}^h : \rho}], \mathbf{C}).$$

The operation $+$ is extended to record types as follows

$$\rho + \rho' \stackrel{\text{def}}{=} \begin{cases} (a[\dots, \mathbf{f}^h : \vartheta_{\mathbf{f}} + \mathbf{f}^{h'} : \vartheta'_{\mathbf{f}}, \dots], \mathbf{C}) & \text{if } \vartheta = (a[\dots, \mathbf{f}^h : \vartheta_{\mathbf{f}}, \dots], \mathbf{C}) \\ & \text{and } \vartheta' = (a[\dots, \mathbf{f}^{h'} : \vartheta'_{\mathbf{f}}, \dots], \mathbf{C}) \\ \top & \text{if } \text{root}(\vartheta) \neq \text{root}(\vartheta') \end{cases}$$

Finally, in the following we will use an extension of method record type that also contain the set of threads that have been joined by the method. Therefore, a *behavioural class table* BCT is a map from pairs $\mathbf{C.m}$ to *method types*, which are terms

$$(\bar{\rho}, t, a) \rightarrow (\nu \bar{a}') \langle \rho, \mathcal{T}, \mathcal{R}, K, \bar{\rho}', \ell \rangle$$

where all the element are as in Section 4, except for K , which is new. This set contains thread names that occur in the arguments (in $\bar{\rho}$) and that have been synchronized within the body of $\mathbf{C.m}$. Correspondingly, we redefine \sqcup as the commutative and associative operator defined by

$$\begin{aligned} (\rho, Z_i, \mathcal{T}_i, \mathcal{R}_i, K_i, \Gamma_i) \sqcup \emptyset &\stackrel{\text{def}}{=} (\rho, Z_i, \mathcal{T}_i, \mathcal{R}_i, K_i, \Gamma_i) \\ (\rho, Z_i, \mathcal{T}_i, \mathcal{R}_i, \Gamma_i) \sqcup (\rho, Z_j, \mathcal{T}_j, \mathcal{R}_j, K_j, \Gamma_j) &\stackrel{\text{def}}{=} (\rho, Z_i, \mathcal{T}_i \cup \mathcal{T}_j, \mathcal{R}_i \cup \mathcal{R}_j, K_i \cap K_j, \Gamma_i + \Gamma_j) \end{aligned}$$

(\sqcup is only defined on tuples with the elements ρ and Z_i equal). We observe that the operation \sqcup when applied to sets K returns their intersection.

C.2 Run-time typing

In the run-time typing rule we use environments that are *sequences of sequences of environments* used at static time. For example we may have $(\Gamma^{1,1} \dots \Gamma^{1,n}) \cdot (\Gamma^{2,1} \dots \Gamma^{2,m})$ (we remind that $\Gamma^{i,j}$ are vectors of maps, which are indexed by addresses. With an abuse of notation, we will range over run-time environments with Γ, Γ', \dots . Whenever Γ is a run-time environment and we write $x \in \text{dom}(\Gamma)$ we mean that there is a map in Γ such that x belongs to the domain of the map. If we write $\Gamma(o) = ([\mathbf{f}_1^{h_1} : \tau_1, \dots, \mathbf{f}_i^{h_i} : \tau_i, \dots, \mathbf{f}_n^{h_n} : \tau_n], \mathbf{C})$ we mean that $o \in \text{dom}(\Gamma)$ and every map γ in Γ such that $o \in \text{dom}(\gamma)$ satisfies $\gamma(o) = ([\mathbf{f}_1^{h_1} : \tau_1, \dots, \mathbf{f}_i^{h_i} : \tau_i, \dots, \mathbf{f}_n^{h_n} : \tau_n], \mathbf{C})$.

► **Definition 4** (Γ - H Agreement). An environment Γ *agrees with* a heap H , written $\Gamma \approx H$ if

- $o \in \text{dom}(H)$ implies $o \in \text{dom}(\Gamma)$ and
- $H(o).class = \mathbf{C}$ and $H(o).\mathbf{f}_i \mapsto v_i$, for $i \in 1..n$, imply $\Gamma(o) = ([\mathbf{f}_1^{h_1} : \tau_1, \dots, \mathbf{f}_i^{h_i} : \tau_i, \dots, \mathbf{f}_n^{h_n} : \tau_n], \mathbf{C})$, where, if v_i is an object name, then $v_i = \tau_i$.

► **Definition 5** (S/F/Z-Agreements). Given Γ , we define the following agreements between the static environments S, F and Z and the corresponding dynamic ones s, f , and z (Γ is always omitted in the notation).

Method invocations

$$\begin{array}{c}
P[i] = \text{invokevirtual C.m } (\mathbf{T}_0, \dots, \mathbf{T}_k) \quad i+1 \in \text{dom}(P) \quad S_i = \tau_k \cdots \tau_0 \cdot S' \\
\text{typeof}(\Gamma_i, \tau_0) = \mathbf{C} \quad \text{mk_tree}(\Gamma_i, (\tau_0, \dots, \tau_k)) = (\rho_0, \dots, \rho_k) \\
\bar{b} = \text{names}(i) \quad \bar{b} \cap \text{var}(\rho_0, \dots, \rho_k, t, [Z_i]) = \emptyset \\
\text{BCT}(\text{C.m})(\rho_0, \dots, \rho_k, t, [Z_i])(\bar{b}) = \langle \rho, \mathcal{T}', \mathcal{R}', K', (\rho'_0, \dots, \rho'_k), \ell \rangle \\
\Gamma_{i+1} = \Gamma_i[\text{env}(\vartheta) + (\bigoplus_{i \in 0..k} \text{env}(\rho'_i))] \\
S_{i+1} = \text{root}(\rho) \cdot S' \quad F_{i+1} = F_i \quad Z_{i+1} = Z_i \quad K_{i+1} = K_i \cup K' \\
\mathcal{T}_{i+1}, \mathcal{R}_{i+1} = \begin{cases} (\mathcal{T}_i \setminus K') \cup \mathcal{T}', (\mathcal{R}_i \setminus K') \cup \mathcal{R}' & \text{if } i \text{ is not recursive and } (\mathcal{T}_i \cup \mathcal{R}_i) \cap (\mathcal{T}' \cup \mathcal{R}') = \emptyset \\ \mathcal{T}_i \setminus K', (\mathcal{R}_i \setminus K') \cup \mathcal{R}' \cup \mathcal{T}' & \text{otherwise} \end{cases} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : \text{C.m}(\rho_0, \dots, \rho_k, t, [Z_i]) \rightarrow \rho \\
\\
P[i] = \text{start C} \quad i+1 \in \text{dom}(P) \quad S_i = t' \cdot S_{i+1} \\
\text{typeof}(\Gamma_i, t') = \mathbf{C} \quad \text{mk_tree}(\Gamma_i, t') = \rho \\
\bar{b} = \text{names}(i) \quad \bar{b} \cap \text{var}(\rho, t', \text{lock}_{t'}) = \emptyset \\
\text{BCT}(\text{C.run})(\rho, t', \text{lock}_{t'}) (\bar{b}) = \langle \text{void}, \mathcal{T}', \mathcal{R}', K', \rho', \ell \rangle \\
\Gamma_{i+1} = \Gamma_i + \text{env}(\rho') \quad F_i = F_{i+1} \quad Z_i = Z_{i+1} \quad K_{i+1} = K_i \cup K' \\
\mathcal{T}_{i+1}, \mathcal{R}_{i+1} = \begin{cases} \mathcal{T}_i \cup \{\tau\} \cup \mathcal{T}', \mathcal{R}_i \cup \mathcal{R}' & \text{if } i \text{ is not recursive} \\ & \text{and } \tau \notin \mathcal{T}_i \cup \mathcal{R}_i \\ \mathcal{T}_i, \mathcal{R}_i \cup \mathcal{R}' \cup \mathcal{T}' \cup \{\tau\} & \text{otherwise} \end{cases} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : \mathbf{0} \\
\\
P[i] = \text{join C} \quad i+1 \in \text{dom}(P) \quad S_i = t' \cdot S_{i+1} \\
\text{typeof}(\Gamma_i, t') = \mathbf{C} \quad \text{mk_tree}(\Gamma_i, t') = \rho \\
\text{joined}(\text{C.run}, \rho, t', \text{lock}_{t'}) = K' \quad \Gamma_{i+1} = \Gamma_i \quad F_i = F_{i+1} \\
Z_i = Z_{i+1} \quad K_{i+1} = \begin{cases} K_i \cup K' & \text{if } \tau \in \mathcal{T}_i \cup \mathcal{R}_i \\ K_i \cup K' \cup \{\tau\} & \text{if } \tau \notin \mathcal{T}_i \cup \mathcal{R}_i \end{cases} \\
\mathcal{T}_{i+1} = (\mathcal{T}_i \setminus K') \setminus \{t'\} \quad \mathcal{R}_{i+1} = (\mathcal{R}_i \setminus K') \setminus \{t'\} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : ([Z_i], \text{lock}_{t'})_t
\end{array}$$

Locking instructions

$$\begin{array}{c}
P[i] = \text{monitorenter} \quad i+1 \in \text{dom}(P) \\
\Gamma_{i+1} = \Gamma_i \quad F_i = F_{i+1} \quad S_i = a \cdot S_{i+1} \\
Z_{i+1} = a \cdot Z_i \quad K_i = K_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : \mathbf{0} \\
\\
P[i] = \text{monitorexit} \quad i+1 \in \text{dom}(P) \\
\Gamma_{i+1} = \Gamma_i \quad F_i = F_{i+1} \quad S_i = a \cdot S_{i+1} \\
Z_{i+1} = Z_i \setminus a \quad K_i = K_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : \mathbf{0}
\end{array}$$

Object manipulation instructions

$$\begin{array}{c}
P[i] = \text{new C} \quad i+1 \in \text{dom}(P) \quad a = \text{names}(i) \quad \text{fields}(\mathbf{C}) = \bar{\mathbf{f}} \quad \Gamma_{i+1} = \Gamma_i[a \mapsto (\overline{[\mathbf{f} : \bar{\top}]}, \mathbf{C})] \\
F_i = F_{i+1} \quad S_{i+1} = a \cdot S_i \quad Z_i = Z_{i+1} \quad K_i = K_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : \widehat{\mathcal{T}}_i \ \& \ \widetilde{\mathcal{R}}_i \ \& \ \widehat{Z}_i^t
\end{array}$$

$$\begin{array}{c}
P[i] = \text{putfield C.f : T} \quad i+1 \in \text{dom}(P) \\
S_i = \tau \cdot a \cdot S_{i+1} \quad \Gamma_i(a) = [\mathbf{f}^h : \tau, \mathbf{f}^h : \tau'] \\
\Gamma_{i+1} = \Gamma_i[a \mapsto [\mathbf{f}^v : \tau, \mathbf{f}^h : \tau']] \quad F_i = F_{i+1} \\
K_i = K_{i+1} \quad Z_i = Z_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : \mathbf{0} \\
\\
P[i] = \text{getfield C.f : T} \quad i+1 \in \text{dom}(P) \\
S_i = a \cdot S' \quad \Gamma_i(a) = [\mathbf{f}^h : \tau, \mathbf{f}^h : \tau'] \\
\Gamma_{i+1} = \Gamma_i[a \mapsto [\mathbf{f}^{h \cup r} : \tau, \mathbf{f}^h : \tau']] \\
S_{i+1} = \tau \cdot S' \quad F_i = F_{i+1} \quad Z_i = Z_{i+1} \\
K_i = K_{i+1} \quad \mathcal{T}_i = \mathcal{T}_{i+1} \quad \mathcal{R}_i = \mathcal{R}_{i+1} \\
\hline
\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : \mathbf{0}
\end{array}$$

Method definitions

$$\begin{array}{c}
\text{BCT}(\text{C.m}) = (\rho_0, \dots, \rho_k, t, a) \rightarrow (\nu \bar{a}') \langle \rho, \mathcal{T}', \mathcal{R}', K', \bar{\rho}', \ell \rangle \\
F_1 = F_{\top}[0 \mapsto \text{root}(\rho_0), \dots, k \mapsto \text{root}(\rho_k)] \\
\Gamma_1 = \bigoplus_{i \in 0..k} \text{env}(\rho_i) \\
S_1 = \varepsilon \quad Z_1 = a \quad \mathcal{T}_1 = \emptyset \quad \mathcal{R}_1 = \emptyset \quad K_1 = \emptyset \\
\left(\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : \ell_i ; \Psi_i \right)_{i \in \text{dom}(P)} \\
\ell = \sum_{i \in \text{dom}(P)} \ell_i \ \& \ \widehat{\mathcal{T}}_i \ \& \ \widetilde{\mathcal{R}}_i \ \& \ \widehat{Z}_i^t \\
\sqcup_{i \in \text{dom}(P)} \Psi_i = (\rho, a, \mathcal{T}', \mathcal{R}', K', \Gamma') \\
\bar{\rho}' = \text{mk_tree}(\Gamma', (\text{root}(\rho_0), \dots, \text{root}(\rho_k))) \\
\bar{a}' = \text{fn}(\rho, \mathcal{T}', \mathcal{R}', K', \bar{\rho}') \setminus \text{fn}(\rho_0, \dots, \rho_k, t, a) \\
\hline
\text{BCT} \vdash \text{T C.m } (\mathbf{T}_1, \dots, \mathbf{T}_k) P \\
\\
\text{BCT}(\text{C.m}) = (a'[\overline{[\mathbf{f} : \bar{\rho}]}], \rho_1, \dots, \rho_k, t, a) \rightarrow (\nu \bar{a}'') \langle \rho, \mathcal{T}', \mathcal{R}', K', \bar{\rho}'', \ell \rangle \\
F_1 = F_{\top}[0 \mapsto a', 1 \mapsto \text{root}(\rho_1), \dots, k \mapsto \text{root}(\rho_k)] \\
\Gamma_1 = \text{env}(a'[\overline{[\mathbf{f} : \bar{\rho}]}]) \oplus (\bigoplus_{j \in 1..k} \text{env}(\rho_j)) \\
S_1 = \varepsilon \quad Z_1 = a' \cdot a \quad \mathcal{T}_1 = \emptyset \quad \mathcal{R}_1 = \emptyset \quad K_1 = \emptyset \\
\left(\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P : \ell_i ; \Psi_i \right)_{i \in \text{dom}(P)} \\
\ell = \sum_{i \in \text{dom}(P)} \ell_i \ \& \ \widehat{\mathcal{T}}_i \ \& \ \widetilde{\mathcal{R}}_i \ \& \ \widehat{Z}_i^t \\
\sqcup_{i \in \text{dom}(P)} \Psi_i = (\rho, a' \cdot a, \mathcal{T}', \mathcal{R}', K', \Gamma') \\
\bar{\rho}'' = \text{mk_tree}(\Gamma', (a', \text{root}(\rho_1), \dots, \text{root}(\rho_k))) \\
\bar{a}'' = \text{fn}(\rho, \mathcal{T}', \mathcal{R}', K', \bar{\rho}'') \setminus \text{fn}(a'[\overline{[\mathbf{f} : \bar{\rho}]}], \rho_1, \dots, \rho_k, t, a) \\
\hline
\text{BCT} \vdash \text{synchronized T C.m } (\mathbf{T}_0, \dots, \mathbf{T}_k) P
\end{array}$$

■ **Figure 8** Type rules for JVML_d programs – extension of those in Figure 3

S-agreement.

- $\epsilon \approx \epsilon$,
- $\tau \cdot S \approx \bullet \cdot s$, if $S \approx s$,
- $\tau \cdot S \approx v \cdot s$, if $S \approx s$ and $\Gamma(v) = \chi$.

F-agreement.

- $\emptyset \approx \emptyset$,
- $F[x \mapsto \tau] \approx f[x \mapsto v]$, if $F \approx_{\Gamma} f$ and $\Gamma(v) = \tau$.

Z-agreement.

- $\epsilon \approx \epsilon$,
- $o \cdot Z \approx o \cdot z$ if $Z \approx z$

Let $\varepsilon \setminus a = \varepsilon$ and

$$(o \cdot z) \setminus a = \begin{cases} z & \text{if } o = a \\ o \cdot (z \setminus a) & \text{otherwise} \end{cases}$$

Let also $z \setminus (a \cdot z')$ be $(z \setminus a) \setminus z'$.

► **Definition 6** (Reachable instructions set). Let $reachable(P, i)$, called the set of addresses reachable from the instruction $i \in dom(P)$, be the least set satisfying the following equations:

$$\begin{aligned} reachable(P, i) &= \emptyset && \text{if } P[i] = \mathbf{return} \\ reachable(P, i) &= \{L\} \cup reachable(P, L) && \text{if } P[i] = \mathbf{goto } L \\ reachable(P, i) &= \{i + 1, L\} \cup reachable(P, i + 1) \cup reachable(P, L) && \text{if } P[i] = \mathbf{if } L \\ reachable(P, i) &= \{i + 1\} \cup reachable(P, i + 1) && \text{otherwise} \end{aligned}$$

In order to type configurations, we need to introduce some notation. We will use sequences of elements \mathcal{T} , \mathcal{R} and K that we will address by using the same notation. The h -th element of the sequence \mathcal{T} will be noted $\mathcal{T}^{(h)}$. Let \mathcal{P} be a sequence of elements whose h -th, noted $\mathcal{P}^{(h)}$, is $((F^{(1,h)}, S^{(1,h)}) \dots (F^{(n_h,h)}, S^{(n_h,h)}), Z^{(h)})$.

The typing judgement for configurations is

$$P, \text{BCT}, \Gamma, \mathcal{P}, \mathcal{T}, \mathcal{R}, K \vdash (\Vdash_H \mathcal{C}) : \ell$$

that is defined by the following two rules:

$$\frac{\text{(T-CONF-AND)} \quad \left(P, \text{BCT}, \Gamma^{(h)}, \mathcal{P}^{(h)}, \mathcal{T}^{(h)}, \mathcal{R}^{(h)}, K^{(h)} \vdash \langle \varphi_h, z_h \rangle_{t_h} : \ell_h \right)^{h \in 1..n} \quad \Gamma \approx H}{P, \text{BCT}, \Gamma, \mathcal{P}, \mathcal{T}, \mathcal{R}, K \vdash (\Vdash_H \langle \varphi_1, z_1 \rangle_{t_1} \dots \langle \varphi_n, z_n \rangle_{t_n}) : \&_{i \in 1..n} \ell_i}$$

(T-CONF-SINGLE)

$$\frac{\left(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[\mathbf{C.m}] : \ell_i; \Psi_i \quad \ell'_i = \ell_i \ \& \ \widehat{\mathcal{T}}'_i \ \& \ \widetilde{\mathcal{R}}'_i \ \& \ \widehat{Z}'_i \right)^{i \in reachable(P[\mathbf{C.m}], pc^{c.m})} \quad \begin{aligned} Z'_{pc^{c.m}} &= \overline{a'} \cdot a & P, \text{BCT}, \Gamma, \mathcal{P}', \mathcal{T}', \mathcal{R}', K \vdash \langle \varphi, z \setminus \overline{a'} \rangle_t : \ell \\ \mathcal{P}' &= ((F^{(1)}, S^{(1)}) \dots (F^{(n)}, S^{(n)}), Z) & \mathcal{P} = ((F', S') \cdot (F^{(1)}, S^{(1)}) \dots (F^{(n)}, S^{(n)}), Z' \cdot Z) \\ Z'_{pc^{c.m}} \cdot Z &\approx z & S'_{pc^{c.m}} \approx s & F'_{pc^{c.m}} \approx f \end{aligned}}{P, \text{BCT}, \Gamma' \cdot \Gamma, \mathcal{P}, \mathcal{T}' \cdot \mathcal{T}, \mathcal{R}' \cdot \mathcal{R}, K' \cdot K \vdash \langle (pc^{c.m}, f, s) \cdot \varphi, z \rangle_t : \sum_{i \in reachable(P[\mathbf{C.m}], pc^{c.m})} \ell'_i + \ell}$$

C.3 Subject Reduction

► **Lemma 7.** *Let $C.m$ be a method of a JVM $_d$ program. If*

$$\text{BCT}(C.m)(\bar{\rho}, t, a) = (\nu \bar{a}') \langle \rho, \mathcal{T}, \mathcal{R}, K, \bar{\rho}', \ell \rangle.$$

then

$$\left(\text{BCT}, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K, i \vdash_t P[C.m] : \ell_i ; \Psi_i \quad \ell'_i = \ell_i \ \& \ \widehat{\mathcal{T}}_i \ \& \ \widetilde{\mathcal{R}}_i \ \& \ \widehat{Z}_i^t \right)^{i \in \text{reachable}(P[C.m], 1^{c.m})},$$

where

$$\begin{aligned} F_1 &= F_{\top}[0 \mapsto \text{root}(\rho_0), \dots, k \mapsto \text{root}(\rho_k)], \\ \Gamma_1 &= \bigoplus_{i \in 0..k} \text{env}(\rho_i), \\ S_1 &= \varepsilon, \\ Z_1 &= A, \quad (\text{if } C.m \text{ is synchronized } A = \text{root}(\rho_0) \cdot a, \text{ otherwise } A = a) \\ \mathcal{T}_1 &= \emptyset, \\ \mathcal{R}_1 &= \emptyset, \\ K_1 &= \emptyset, \\ \ell &\geq \sum_{i \in \text{reachable}(P[C.m], 1^{c.m})} \ell'_i, \\ \bigsqcup_{i \in \text{reachable}(P[C.m], 1^{c.m})} \Psi_i &= (\rho, A, \mathcal{T}', \mathcal{R}', K', \Gamma'), \quad \mathcal{T}' \subseteq \mathcal{T}, \quad \mathcal{R}' \subseteq \mathcal{R}, \quad K' \supseteq K \\ \bar{\rho}' &= \text{mk_tree}(\Gamma', (\text{root}(\rho_0), \dots, \text{root}(\rho_k))), \text{ and} \\ \bar{a}' &\supseteq \text{fn}(\rho, \mathcal{T}', \mathcal{R}', K', \bar{\rho}') \setminus \text{fn}(\rho_0, \dots, \rho_k, t, a). \end{aligned}$$

► **Theorem 8** (Subject Reduction). *If $P, \text{BCT}, \Gamma, \mathcal{P}, \mathcal{T}, \mathcal{R}, K \vdash (\Vdash_H \mathcal{C}) : \ell$ and $\Gamma \approx H$ and $\Vdash_H \mathcal{C} \rightarrow \Vdash_{H'} \mathcal{C}'$, then there exists ℓ' and $\Gamma', \mathcal{P}', \mathcal{T}', \mathcal{R}', K'$ such that $\Gamma' \approx H'$ and $P, \text{BCT}, \Gamma', \mathcal{P}', \mathcal{T}', \mathcal{R}', K' \vdash (\Vdash_{H'} \mathcal{C}') : \ell'$ and $\ell \geq \ell'$.*

Proof. (Sketch) By case analysis on the last reduction rule used.

Case pop. We have $P, \text{BCT}, \Gamma, \mathcal{P}, \mathcal{T}, \mathcal{R}, K \vdash (\Vdash_H \langle (pc^{c.m}, f, v \cdot s) \cdot \varphi, z \rangle_t) : \ell$ and

$$\frac{\text{(s-POP)} \quad P[pc^{c.m}] = \text{pop}}{P \Vdash_H \langle (pc^{c.m}, f, v \cdot s) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (pc^{c.m} + 1, f, s) \cdot \varphi, z \rangle_t}$$

By the configuration typing rules, we get

$$\begin{aligned} \left(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[C.m] : \ell_i \quad \ell'_i = \ell_i \ \& \ \widehat{\mathcal{T}}'_i \ \& \ \widetilde{\mathcal{R}}'_i \ \& \ \widehat{Z}'_i^t \right)^{i \in \text{reachable}(P[C.m], pc^{c.m})} \\ P, \text{BCT}, \Gamma'', \mathcal{P}'', \mathcal{T}'', \mathcal{R}'', K'' \vdash \langle \varphi, z \rangle_t : \ell_{\varphi} \quad \mathcal{P}'' = \left((F'', S''), Z'' \right) \\ Z'_{pc^{c.m}} = \bar{a}' \cdot a \quad \bar{a}' \cdot Z'' \approx z \quad S'_{pc^{c.m}} \approx v \cdot s \quad F'_{pc^{c.m}} \approx f \end{aligned}$$

From $(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[C.m] : \ell_i)^{i \in \text{reachable}(P[C.m], pc^{c.m})}$ and Definition 6 we derive that

$$\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', pc^{c.m} \vdash_t P[C.m] : \ell_{pc^{c.m}}$$

and

$$(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[C.m] : \ell_i)^{i \in \text{reachable}(P[C.m], pc^{c.m} + 1)}.$$

Therefore $\ell = \ell'_{pc^{c.m}} + (\sum_{i \in \text{reachable}(P[C.m], pc^{c.m} + 1)} \ell'_i) + \ell_{\varphi}$. By the typing rule for **pop**: $S'_{pc^{c.m}} = \tau \cdot S'_{pc^{c.m} + 1}$; therefore, since $S'_{pc^{c.m}} \approx v \cdot s$ then $S'_{pc^{c.m} + 1} \approx s$, by Definition 5.

We can apply again the configuration typing rules and we obtain $\ell' = (\sum_{i \in \text{reachable}(P[C.m], pc^{c.m} + 1)} \ell'_i) + \ell_{\varphi}$, thus $\ell \geq \ell'$.

Case invokevirtual. We have $P, \text{BCT}, \Gamma, \mathcal{P}, \mathcal{T}, \mathcal{R}, K \vdash (\llbracket_H \langle (pc^{c.m}, f, v_n \cdot \dots \cdot v_1 \cdot o \cdot s) \cdot \varphi, z \rangle_t : \ell \text{ and one of three operational rules may have been applied: (S-INVK), (S-INVK-SYNCH-0), or (S-INVK-SYNCH-N). Let us consider the former:$

$$\begin{array}{c} \text{(S-INVK)} \\ \frac{P[pc^{c.m}] = \text{invokevirtual } D.m'(T_1, \dots, T_n) \quad \text{synchronized} \notin \text{mod}(D.m')}{P \llbracket_H \langle (pc^{c.m}, f_1, v_n \cdot \dots \cdot v_1 \cdot o \cdot s_1) \cdot \varphi, z \rangle_t \rightarrow \llbracket_H \langle (1^{D.m'}, f_2[0 \mapsto o, 1 \mapsto v_1, \dots, n \mapsto v_n], \epsilon) \cdot (pc^{c.m} + 1, f_1, \bullet \cdot s_1) \cdot \varphi, z \rangle_t} \end{array}$$

By the configuration typing rules, we get

$$\begin{aligned} & \left(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[\text{C.m}] : \ell_i \quad \ell'_i = \ell_i \ \& \ \widehat{\mathcal{T}}'_i \ \& \ \widetilde{\mathcal{R}}'_i \ \& \ \widehat{Z}'_i \right)^{i \in \text{reachable}(P[\text{C.m}], pc^{c.m})} \\ & P, \text{BCT}, \Gamma'', \mathcal{P}'', \mathcal{T}'', \mathcal{R}'', K'' \vdash \langle \varphi, z \rangle_t : \ell_\varphi \quad \mathcal{P}'' = \left(\overline{(F'', S'')}, Z'' \right) \\ & Z'_{pc^{c.m}} = \overline{a'} \cdot a \quad \overline{a'} \cdot Z'' \approx z \quad S'_{pc^{c.m}} \approx v_n \cdot \dots \cdot v_1 \cdot o \cdot s \quad F'_{pc^{c.m}} \approx f \end{aligned}$$

From $(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[\text{C.m}] : \ell_i)^{i \in \text{reachable}(P[\text{C.m}], pc^{c.m})}$ and Definition 6 we derive that

$$\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', pc^{c.m} \vdash_t P[\text{C.m}] : \ell_{pc^{c.m}}$$

and

$$\left(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[\text{C.m}] : \ell_i \right)^{i \in \text{reachable}(P[\text{C.m}], pc^{c.m} + 1)}.$$

Therefore

$$\ell = \ell'_{pc^{c.m}} + \left(\sum_{i \in \text{reachable}(P[\text{C.m}], pc^{c.m} + 1)} \ell'_i \right) + \ell_\varphi. \quad (2)$$

We assume that C.m is recursive (this case and the one when $pc^{c.m}$ is inside an iteration are the interesting cases). By (T-INVK) typing rule:

$$\ell_{pc^{c.m}} = D.m'(\rho_1, \dots, \rho_n, t, [Z'_{pc^{c.m}}]) \rightarrow \rho,$$

where

$$\begin{aligned} S'_{pc^{c.m}} &= \tau_n \cdot \dots \cdot \tau_1 \cdot S''' \quad \text{typeof}(\Gamma_{pc^{c.m}}, \tau_1) = D \\ mk_tree(\Gamma_{pc^{c.m}}, (\tau_1, \dots, \tau_n)) &= (\rho_1, \dots, \rho_n) \quad \bar{b} = \text{names}(pc^{c.m}) \\ \text{BCT}(D.m')(\rho_1, \dots, \rho_n, t, [Z'_{pc^{c.m}}]) &(\bar{b}) = \langle \rho, \mathcal{T}''', \mathcal{R}''', K''', (\rho'_1, \dots, \rho'_n), \ell_{D.m'} \rangle \\ \Gamma_{pc^{c.m}+1} &= \Gamma_{pc^{c.m}}[\text{env}(\rho) + (\bigoplus_{i=1..n} \text{env}(\rho'_i))] \\ S'_{pc^{c.m}+1} &= \text{root}(\rho) \cdot S''' \quad F'_{pc^{c.m}+1} = F'_{pc^{c.m}} \quad Z'_{pc^{c.m}+1} = Z'_{pc^{c.m}} \quad K'_{pc^{c.m}+1} = K'_{pc^{c.m}} \cup K''' \\ \mathcal{T}'_{pc^{c.m}+1}, \mathcal{R}'_{pc^{c.m}+1} &= (\mathcal{T}'_{pc^{c.m}} \setminus K''') \cup \mathcal{T}''', (\mathcal{R}'_{pc^{c.m}} \setminus K''') \cup \mathcal{R}''' \end{aligned}$$

By (T-INVK) typing rule, $S'_{pc^{c.m}} = \tau_n \cdot \dots \cdot \tau_1 \cdot S'''$ and $S'_{pc^{c.m}+1} = \text{root}(\rho) \cdot S'''$ so since $S'_{pc^{c.m}} \approx v_n \cdot \dots \cdot v_1 \cdot o \cdot s$ then $S'_{pc^{c.m}+1} \approx \bullet \cdot s$.

Therefore we obtain that

$$P, \text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K' \vdash \langle (pc^{c.m} + 1, f, \bullet \cdot s) \cdot \varphi, z \rangle_t : \ell_{pc^{c.m}+1} \quad (3)$$

where $\ell_{pc^{c.m}+1} = \left(\sum_{i \in \text{reachable}(P[\text{C.m}], pc^{c.m} + 1)} \ell'_i \right) + \ell_\varphi$

By Lemma 7,

$$\left(\text{BCT}, \Gamma^{D.m'}, F^{D.m'}, S^{D.m'}, Z^{D.m'}, \mathcal{T}^{D.m'}, \mathcal{R}^{D.m'}, K^{D.m'}, i \vdash_t P[D.m'] : \ell_i^{D.m'} ; \Psi_i \right)^{i \in \text{reachable}(P[D.m'], 1^{D.m'})},$$

where

$$\begin{aligned}
F_1^{\mathbb{D}.m'} &= F_{\top}^{\mathbb{D}.m'}[0 \mapsto \text{root}(\rho_1), \dots, n \mapsto \text{root}(\rho_n)], \\
\Gamma_1^{\mathbb{D}.m'} &= \bigoplus_{i \in 1..n} \text{env}(\rho_i), \\
S_1^{\mathbb{D}.m'} &= \varepsilon, \\
Z_1^{\mathbb{D}.m'} &= [Z'_{pc^{c.m}}], \\
\mathcal{T}_1^{\mathbb{D}.m'} &= \emptyset, \\
\mathcal{R}_1^{\mathbb{D}.m'} &= \emptyset, \\
K_1^{\mathbb{D}.m'} &= \emptyset, \\
\ell_{\mathbb{D}.m'} &\geq \ell'', \quad \text{where } \ell'' = \sum_{i \in \text{reachable}(P[\mathbb{D}.m'], 1^{\mathbb{D}.m'})} \ell_i^{\mathbb{D}.m'} \& \widehat{\mathcal{T}}_i^{\mathbb{D}.m'} \& \widetilde{\mathcal{R}}_i^{\mathbb{D}.m'} \& \widehat{Z}_i^{\mathbb{D}.m' t}, \\
\prod_{i \in \text{reachable}(P[\mathbb{D}.m'], 1^{\mathbb{D}.m'})} \Psi_i &= (\rho, [Z'_{pc^{c.m}}], \mathcal{T}^{\mathbb{D}.m'}, \mathcal{R}^{\mathbb{D}.m'}, K^{\mathbb{D}.m'}, \Gamma^{\mathbb{D}.m'}), \\
\bar{\rho}' &= \text{mk_tree}(\Gamma^{\mathbb{D}.m'}, (\text{root}(\rho_1), \dots, \text{root}(\rho_n))), \text{ and} \\
\bar{b} &= \text{fn}(\rho, \mathcal{T}^{\mathbb{D}.m'}, \mathcal{R}^{\mathbb{D}.m'}, K^{\mathbb{D}.m'}, \bar{\rho}') \setminus \text{fn}(\rho_1, \dots, \rho_n, t, a).
\end{aligned}$$

Thus

$$\begin{aligned}
P, \text{BCT}, \Gamma^{\mathbb{D}.m'} \cdot \Gamma', F^{\mathbb{D}.m'} \cdot F', S^{\mathbb{D}.m'} \cdot S', Z^{\mathbb{D}.m'} \cdot Z', \mathcal{T}^{\mathbb{D}.m'} \cdot \mathcal{T}', \mathcal{R}^{\mathbb{D}.m'} \cdot \mathcal{R}', K^{\mathbb{D}.m'} \cdot K' \vdash \\
\langle \langle (1^{\mathbb{D}.m'}, f'[0 \mapsto o, 1 \mapsto v_1, \dots, n \mapsto v_n], \epsilon) \cdot (pc^{c.m} + 1, f, s) \cdot \varphi, z \rangle_t \rangle : \ell'' + \ell_{pc^{c.m}+1}
\end{aligned}$$

and $\ell \geq \ell'' + \ell_{pc^{c.m}+1}$.

The last step is to prove that $\Gamma^{\mathbb{D}.m'} \cdot \Gamma \approx H$. This follows from Definition 4 and the fact that $\Gamma \approx H$.

Case start. We have $P, \text{BCT}, \Gamma, \mathcal{P}, \mathcal{T}, \mathcal{R}, K \vdash (\Vdash_H \langle (pc^{c.m}, f_1, o \cdot s_1) \cdot \varphi, z \rangle_t) : \ell$ and then

$$\begin{array}{c}
\text{(S-START)} \\
\hline
P[pc^{c.m}] = \text{start D} \\
\hline
P \Vdash_H \langle (pc^{c.m}, f_1, o \cdot s_1) \cdot \varphi, z \rangle_t \rightarrow \Vdash_H \langle (pc^{c.m} + 1, f_1, s_1) \cdot \varphi, z \rangle_t, \langle (1^{\mathbb{D}.run}, f_2[0 \mapsto o], \epsilon), \epsilon \rangle_o
\end{array}$$

By the configuration typing rules, we obtain

$$\begin{aligned}
& \left(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[\mathbf{C.m}] : \ell_i \quad \ell'_i = \ell_i \& \widehat{\mathcal{T}}'_i \& \widetilde{\mathcal{R}}'_i \& \widehat{Z}'_i \right)_{i \in \text{reachable}(P[\mathbf{C.m}], pc^{c.m})} \\
& P, \text{BCT}, \Gamma'', \mathcal{P}'', \mathcal{T}'', \mathcal{R}'', K'' \vdash \langle \varphi, z \rangle_t : \ell_\varphi \quad \mathcal{P}'' = \left(\overline{(F'', S'')}, Z'' \right) \\
& Z'_{pc^{c.m}} = \bar{a}' \cdot a \quad \bar{a}' \cdot Z'' \approx z \quad S'_{pc^{c.m}} \approx o \cdot s \quad F'_{pc^{c.m}} \approx f
\end{aligned}$$

From $(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[\mathbf{C.m}] : \ell_i)_{i \in \text{reachable}(P[\mathbf{C.m}], pc^{c.m})}$ and Definition 6 we derive that

$$\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', pc^{c.m} \vdash_t P[\mathbf{C.m}] : \ell_{pc^{c.m}}$$

and

$$(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[\mathbf{C.m}] : \ell_i)_{i \in \text{reachable}(P[\mathbf{C.m}], pc^{c.m}+1)}.$$

Therefore

$$\ell = \ell'_{pc^{c.m}} + \left(\sum_{i \in \text{reachable}(P[\mathbf{C.m}], pc^{c.m}+1)} \ell'_i \right) + \ell_\varphi. \tag{4}$$

By (T-START) typing rule:

$$\begin{aligned}
& S'_{pc^{c.m}} = o \cdot S'_{pc^{c.m}+1} \\
& \text{typeof}(\Gamma'_{pc^{c.m}}, o) = \text{D} \quad \text{mk_tree}(\Gamma'_{pc^{c.m}}, o) = \rho \quad \bar{b} = \text{names}(pc^{c.m}) \\
& \text{BCT}(\text{D.run})(\rho, o, \text{lock}_o)(\bar{b}) = \langle \text{void}, \mathcal{T}''', \mathcal{R}''', K''', \rho', \ell \rangle \\
& \Gamma'_{pc^{c.m}+1} = \Gamma'_{pc^{c.m}} + \text{env}(\rho') \quad F'_{pc^{c.m}} = F'_{pc^{c.m}+1} \quad Z'_{pc^{c.m}} = Z'_{pc^{c.m}+1} \quad K'_{pc^{c.m}+1} = K'_{pc^{c.m}} \\
& \mathcal{T}_{pc^{c.m}+1}, \mathcal{R}_{pc^{c.m}+1} = \begin{cases} \mathcal{T}_{pc^{c.m}} \cup \{o\} \cup \mathcal{T}''', \mathcal{R}_{pc^{c.m}} \cup \mathcal{R}''' & \text{if } i \text{ is not recursive} \\ & \text{and } o \notin \mathcal{T}_{pc^{c.m}} \cup \mathcal{R}_{pc^{c.m}} \\ \mathcal{T}_{pc^{c.m}}, \mathcal{R}_{pc^{c.m}} \cup \mathcal{R}''' \cup \mathcal{T}''' \cup \{o\} & \text{otherwise} \end{cases}
\end{aligned}$$

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Let $S'_{pc^{c.m}} \approx o \cdot s_1$; then $S'_{pc^{c.m+1}} \approx s_1$. Therefore we obtain that

$$P, \text{BCT}, \Gamma', \Gamma'', \mathcal{P}', \mathcal{T}', \mathcal{T}'', \mathcal{R}' \cdot \mathcal{R}'', K' \cdot K'' \vdash \langle (pc^{c.m} + 1, f_1, s_1) \cdot \varphi, z \rangle_t : \ell_{pc^{c.m+1}} \quad (5)$$

where $\mathcal{P}' = \left((F', S') \cdot \overline{(F'', S'')}, \overline{a'} \cdot Z'' \right)$ and $\ell_{pc^{c.m+1}} = \left(\sum_{i \in \text{reachable}(P[\text{C.m}], pc^{c.m+1})} \ell'_i \right) + \ell_\varphi$. By Lemma 7,

$$\left(\text{BCT}, \Gamma^{\text{D.run}}, F^{\text{D.run}}, S^{\text{D.run}}, Z^{\text{D.run}}, T^{\text{D.run}}, K^{\text{D.run}}, i \vdash_t P[\text{D.run}] : \ell_i^{\text{D.run}} ; \Psi_i \right)^{i \in \text{reachable}(P[\text{D.run}], 1^{\text{D.run}})},$$

where

$$\begin{aligned} F_1^{\text{D.run}} &= F_{\perp}^{\text{D.run}}[0 \mapsto \text{root}(\rho)], \\ \Gamma_1^{\text{D.run}} &= \text{env}(\rho), \\ S_1^{\text{D.run}} &= \varepsilon, \\ Z_1^{\text{D.run}} &= [Z'_{pc^{c.m}}], \\ \mathcal{T}_1^{\text{D.run}} &= \emptyset, \\ \mathcal{R}_1^{\text{D.run}} &= \emptyset, \\ K_1^{\text{D.run}} &= \emptyset, \\ \ell^{\text{D.run}} &= \sum_{i \in \text{reachable}(P[\text{D.run}], 1^{\text{D.run}})} (\ell_i^{\text{D.run}} \& \widehat{\mathcal{T}}_i^{\text{D.run}} \& \widetilde{\mathcal{R}}_i^{\text{D.run}} \& \widehat{Z}_i^{\text{D.run}t}), \\ \bigsqcup_{i \in \text{reachable}(P[\text{D.run}], 1^{\text{D.run}})} \Psi_i &= (\text{void}, [Z'_{pc^{c.m}}], \mathcal{T}^{\text{D.run}}, \mathcal{R}^{\text{D.run}}, K^{\text{D.run}}, \Gamma^{\text{D.run}}), \\ \vartheta &= \text{mk_tree}(\Gamma^{\text{D.run}}, o), \\ \bar{b} &= \text{fn}(\text{void}, \mathcal{T}^{\text{D.run}}, \mathcal{R}^{\text{D.run}}, K^{\text{D.run}}, \vartheta) \setminus \text{fn}(\rho, o, \text{lock}_o). \end{aligned}$$

Thus, let $\mathcal{P}''' = \mathcal{P}' \cdot ((F^{\text{D.run}}, S^{\text{D.run}}), Z_1^{\text{D.run}})$, then

$$P, \text{BCT}, (\Gamma' \cdot \Gamma'') \cdot \Gamma^{\text{D.run}}, \mathcal{P}''', (\mathcal{T}' \cdot \mathcal{T}'') \cdot \mathcal{T}^{\text{D.run}}, (\mathcal{R}' \cdot \mathcal{R}'') \cdot \mathcal{R}^{\text{D.run}}, (K' \cdot K'') \cdot K^{\text{D.run}} \vdash \langle \llbracket_H \langle (pc^{c.m} + 1, f_1, s_1) \cdot \varphi, z \rangle_t, \langle (1^{\text{D.run}}, f_2[0 \mapsto o], \epsilon), \epsilon \rangle_o \rangle : \ell^{\text{D.run}} \& \ell_{pc^{c.m+1}}.$$

and $\ell \geq \ell^{\text{D.run}} \& \ell_{pc^{c.m+1}}$. The last step, namely $(\Gamma' \cdot \Gamma'') \cdot \Gamma^{\text{D.run}} \approx H$, is similar to the previous case.

Case return. We have

$$P, \text{BCT}, \Gamma, \mathcal{P}, \mathcal{T}, \mathcal{R}, K \vdash \langle \llbracket_H \langle (pc^{c.m}, f_1, v \cdot s_1) \cdot (pc^{\text{D.m}}, f_2, \bullet \cdot s_2) \cdot \varphi, z \rangle_t \rangle : \ell$$

The following rules may have been applied: (S-RETURN), (S-RETURN-RUN), (S-RETURN-SYNCH-0), and (S-RETURN-SYNCH-N). Let us consider the first one (the other ones are similar).

$$\frac{\text{(S-RETURN)} \quad P[pc^{c.m}] = \text{return} \quad \text{synchronized} \notin \text{mod}(\text{C.m})}{P \llbracket_H \langle (pc^{c.m}, f_1, v \cdot s_1) \cdot (pc^{\text{D.m}}, f_2, \bullet \cdot s_2) \cdot \varphi, z \rangle_t \rightarrow \llbracket_H \langle pc^{\text{D.m}}, f_2, v \cdot s_2 \rangle \cdot \varphi, z \rangle_t}$$

By the configuration typing rules, we have

$$\begin{aligned} \left(\text{BCT}, \Gamma', F', S', Z', \mathcal{T}', \mathcal{R}', K', i \vdash_t P[\text{C.m}] : \ell_i \quad \ell'_i = \ell_i \& \widehat{\mathcal{T}}'_i \& \widetilde{\mathcal{R}}'_i \& \widehat{Z}'_i \right)^{i \in \text{reachable}(P[\text{C.m}], pc^{c.m})} \\ P, \text{BCT}, \Gamma'', \mathcal{P}'', \mathcal{T}'', \mathcal{R}'', K'' \vdash \langle (pc^{\text{D.m}}, f_2, \bullet \cdot s_2) \cdot \varphi, z \rangle_t : \ell_\varphi \quad \mathcal{P}'' = \left(\overline{(F'', S'')}, Z'' \right) \\ Z'_{pc^{c.m}} = \overline{a'} \cdot a \quad \overline{a'} \cdot Z'' \approx z \quad S'_{pc^{c.m}} \approx v \cdot s_1 \quad F'_{pc^{c.m}} \approx f_1 \end{aligned}$$

Therefore

$$\ell = \left(\sum_{i \in \text{reachable}(P[\text{C.m}], pc^{c.m})} \ell'_i \right) + \left(\sum_{i \in \text{reachable}(P[\text{D.m}], pc^{\text{D.m}})} \ell'_i \right) + \ell_\varphi \quad (6)$$

Moreover, $S''_{pc^{D.m'}} \approx \bullet \cdot s_2$ and $S''_{pc^{D.m'}}$ has been set while typing the invocation of $C.m$ as $S''_{pc^{D.m'}} = \text{root}(\theta) \cdot S'''$, where $\text{root}(\theta)$ is the value returned by the method invocation, then $S''_{pc^{D.m'}} \approx v \cdot s_2$.

Therefore we obtain

$$\ell' = \left(\sum_{i \in \text{reachable}(P[D.m], pc^{D.m})} \ell'_i \right) + \ell_\varphi \quad (7)$$

then $\ell \geq \ell'$.

Case new. We have

$$P, \text{BCT}, \Gamma, \mathcal{P}, \mathcal{I}, \mathcal{R}, K \vdash (\|_{-H} \langle (pc^{C.m}, f, s) \cdot \varphi, z \rangle_t) : \ell$$

and

$$\frac{\text{(S-NEW)} \quad P[pc^{C.m}] = \text{new } D \quad o \notin \text{dom}(H) \quad H' = H[o \mapsto (\rho^D, D)]}{P \Vdash_H \langle (pc^{C.m}, f, s) \cdot \varphi, z \rangle_t \rightarrow \|_{-H'} \langle (pc^{C.m} + 1, f, o \cdot s) \cdot \varphi, z \rangle_t}$$

By the configuration typing rules, we get

$$\left(\text{BCT}, \Gamma', F', S', Z', \mathcal{I}', \mathcal{R}', K', i \vdash_t P[C.m] : \ell_i \quad \ell'_i = \ell_i \ \& \ \widehat{\mathcal{I}}'_i \ \& \ \widetilde{\mathcal{R}}'_i \ \& \ \widehat{Z}'_i \right)_{i \in \text{reachable}(P[C.m], pc^{C.m})} \\ P, \text{BCT}, \Gamma'', \mathcal{P}'', \mathcal{I}'', \mathcal{R}'', K'' \vdash \langle \varphi, z \setminus Z'' \rangle_t : \ell_\varphi \quad \mathcal{P}'' = \left(\overline{F''}, S'' \right), Z'' \\ Z'_{pc^{C.m}} = \overline{a'} \cdot a \quad \overline{a'} \cdot Z'' \approx z \quad S''_{pc^{C.m}} \approx s \quad F''_{pc^{C.m}} \approx f$$

Therefore

$$\ell = \left(\sum_{i \in \text{reachable}(P[C.m], pc^{C.m})} \ell'_i \right) + \ell_\varphi \quad (8)$$

From $(\text{BCT}, \Gamma', F', S', Z', \mathcal{I}', \mathcal{R}', K', i \vdash_t P[C.m] : \ell_i)_{i \in \text{reachable}(P[C.m], pc^{C.m})}$ and Definition 6 we derive that

$$\text{BCT}, \Gamma', F', S', Z', \mathcal{I}', \mathcal{R}', K', pc^{C.m} \vdash_t P[C.m] : \ell_{pc^{C.m}}$$

and

$$\left(\text{BCT}, \Gamma', F', S', Z', \mathcal{I}', \mathcal{R}', K', i \vdash_t P[C.m] : \ell_i \right)_{i \in \text{reachable}(P[C.m], pc^{C.m}+1)}.$$

Therefore $\ell = \ell'_{pc^{C.m}} + \left(\sum_{i \in \text{reachable}(P[C.m], pc^{C.m}+1)} \ell'_i \right) + \ell_\varphi$.

By these hypotheses, it is easy to show the existence of $\Gamma''', \mathcal{P}', \mathcal{I}'', \mathcal{R}'', K''$ and ℓ'' such that $\Gamma''' \approx H'$ and $P, \text{BCT}, \Gamma''', \mathcal{P}', \mathcal{I}'', \mathcal{R}'', K'' \vdash (\|_{-H'} \langle (pc^{C.m} + 1, f, o \cdot s) \cdot \varphi, z \rangle_t) : \ell''$. In particular, assuming $\text{fields}(D) = \overline{f}$, it is sufficient to let $\Gamma''' = \Gamma'[o \mapsto (\overline{[f : \overline{T}]}, D)] \cdot \Gamma''$ and $S''_{pc^{C.m}+1} = o \cdot S''_{pc^{C.m}}$.

In this case, it is not evident that $\ell \geq \ell'$. Actually, by the typing rule for **new**, the new name is a symbolic name returned by the function $\text{names}(pc^{C.m})$, let it be a . Therefore, $\Gamma'_{pc^{C.m}+1} = \Gamma'_{pc^{C.m}}[a \mapsto (\overline{[f : \overline{T}]}, D)]$, and $S'_{pc^{C.m}+1} = a \cdot S''_{pc^{C.m}}$. However, we cannot assume that $a = o$ because it may be already “in use” – this is the case when $C.m$ is either recursive or $pc^{C.m}$ is inside an iteration.

There are two cases: (i) D is not a subclass of **Thread** and (ii) D is a subclass of **Thread**. In case (i) it easy to verify that $\ell \geq \ell' \{a/o\} \geq \ell'$. In case (ii) we may have $\ell' = (b, c)_a \ \& \ (c, b)_o$.

When ℓ' has such a value, we have $\ell'\{a/o\} \not\geq \ell'$ because the name o may appear as index of a dependency pair. In such situation, $(b, c)_a \ \& \ (c, b)_o$ is a circular dependency, while $\ell'\{a/o\} = (b, c)_a \ \& \ (c, b)_a$ is not – see Sections 5 and C.

To solve this criticality, whenever a new thread is created – either because a **start** is executed or because it is returned by a method invocation, *cf.* the sets \mathcal{T} and \mathcal{R} – we verify whether we are inside a recursion or an iteration, which are the two cases when $names(pc^{c.m})$ may return a name that already exists. In these two cases, the name a is added to the set \mathcal{R} instead of \mathcal{T} . As a consequence, the lam ℓ' will contain terms $RUN((o[\overline{f} : \rho], D))$ while, correspondingly, ℓ will contain terms $RUN((o[\overline{f} : \rho], D))\{a/o\}$. However, since

$$RUN((o[\overline{f} : \rho], D)) = D.run((o[\overline{f} : \rho], D), o, lock_o) \ \& \ (\nu o') \ RUN((o'[\overline{f} : \rho], D)) ,$$

replacing o in $RUN((o[\overline{f} : \rho], D))$ with any other object name does not change the circularity predicate (because the function is unfolded in a lam that has an invocation of the same function on a fresh name in parallel). Therefore, it is possible to derive $\ell\{a/i\} \geq \ell$. ◀

C.4 Deadlocks and circularities

To conclude the proof of correctness we need to bridge the gap between the notion of deadlock in JVM and the notion of circularity in a lam program.

► **Definition 9.** A JVM configuration $\Vdash_H \langle \varphi_1, z_1 \rangle_{t_1} \cdots \langle \varphi_n, z_n \rangle_{t_n}$ is *deadlocked* if there are $i_1, \dots, i_k \in 1..n$ such that, for every $j \in \{i_1, \dots, i_k\}$ one of the following holds

- $\varphi_j = (pc_j, f_j, s_j) \cdot \varphi'_j$ and $P[pc_j] = \mathbf{monitorenter}$ and $s_j = a \cdot s'_j$ and $a \in z_h$ with $h \in \{i_1, \dots, i_k\} \setminus j$;
- $\varphi_j = (pc_j, f_j, s_j) \cdot \varphi'_j$ and $P[pc_j] = \mathbf{join}$ and $s_j = t_h \cdot s'_j$ with $h \in \{i_1, \dots, i_k\}$.

The following statement is a straightforward consequence of the definitions.

► **Proposition 10.** Let $\Vdash_H \mathcal{C}$ be deadlocked and let $P, BCT, \Gamma, F, S, Z, \mathcal{T}, \mathcal{R}, K \vdash (\Vdash_H \mathcal{C}) : \ell$. Then ℓ has a circularity.

► **Theorem 11.** Let P be a $JVML_d$ program and $\Gamma \approx H, F \approx_\Gamma f_\perp[0 \mapsto main], S \approx_\Gamma \epsilon$, and $Z \approx_\Gamma \epsilon$. If

1. $P, BCT, \Gamma, \mathcal{P}, \mathcal{T}, \mathcal{R}, K \vdash (\Vdash_H \langle (1^{c.main}, f_\perp[0 \mapsto main]), \epsilon \rangle_{main}) : \ell$
2. and $\Vdash_H \langle (1^{c.main}, f_\perp[0 \mapsto main]), \epsilon \rangle_{main} \rightarrow^* \Vdash_{H'} \mathcal{C}'$
3. and $\Vdash_{H'} \mathcal{C}'$ is deadlocked

then ℓ has a circularity.

Proof. The proof is an immediate consequence of the previous results. By 1 and 2 and Theorem 8 we have the existence of ℓ' and $\Gamma', \mathcal{P}', \mathcal{T}', \mathcal{R}', K'$ such that $\Gamma' \approx H'$ and $P, BCT, \Gamma', \mathcal{P}', \mathcal{T}', \mathcal{R}', K' \vdash (\Vdash_{H'} \mathcal{C}') : \ell'$ and $\ell \geq \ell'$. By 3 and Proposition 10, we have that ℓ' has a circularity. By $\ell \geq \ell'$ and Lemma 10, we have that ℓ has a circularity as well. ◀